

Fig. 1A

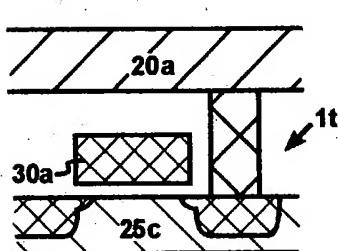


Fig. 1B

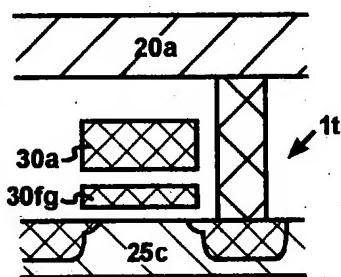


Fig. 1DA

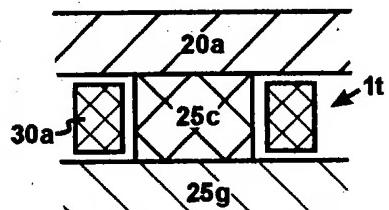


Fig. 1DB

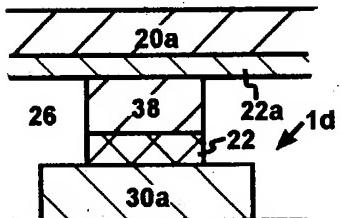


Fig. 1C

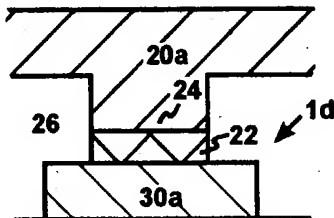


Fig. 1EA

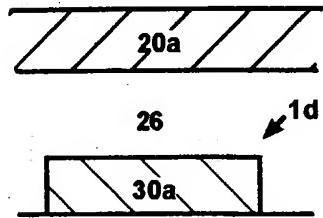


Fig. 1EB

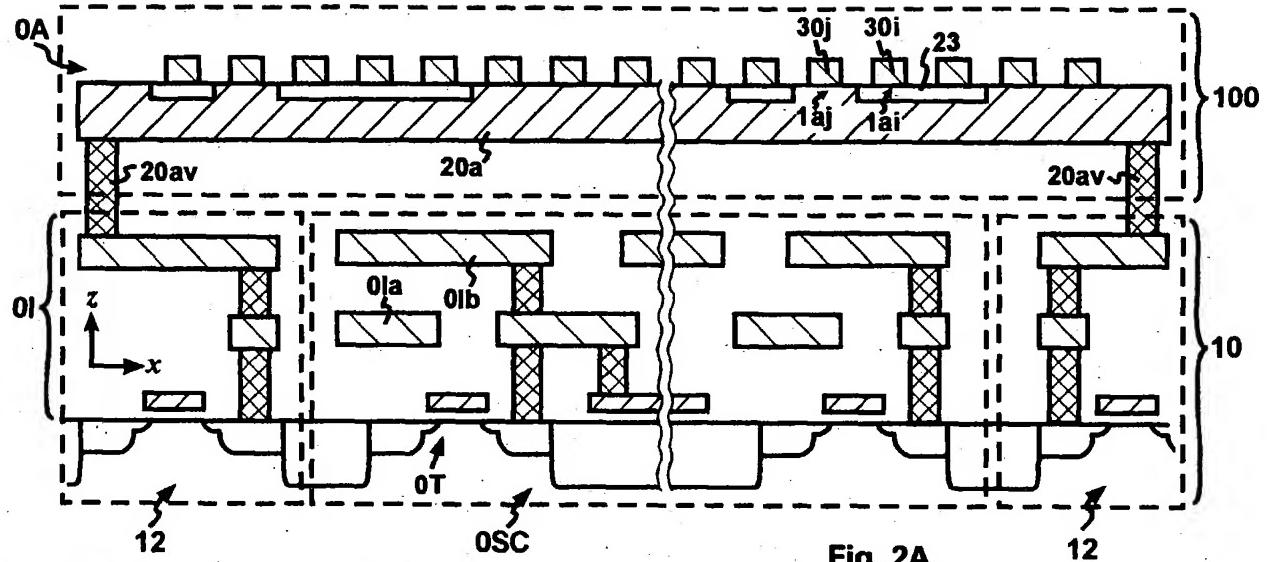


Fig. 2A

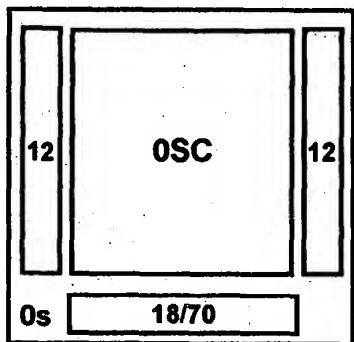


Fig. 2B

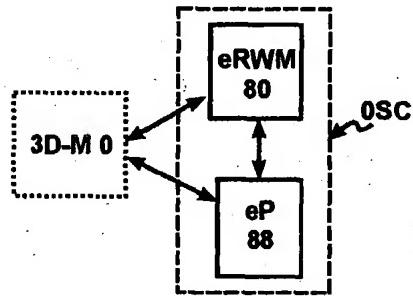


Fig. 2C

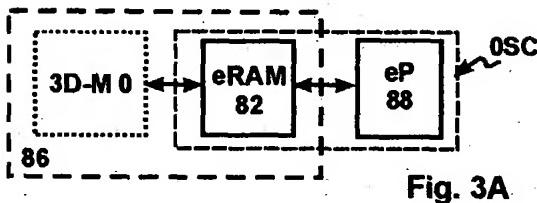


Fig. 3A

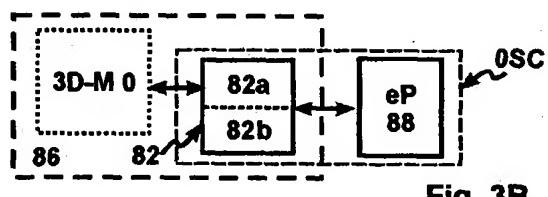


Fig. 3B

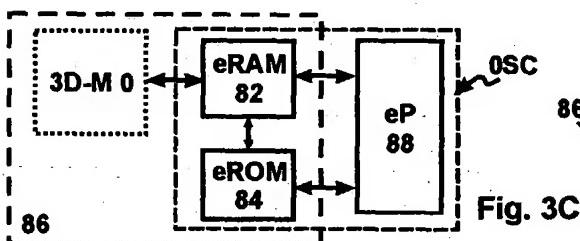


Fig. 3C

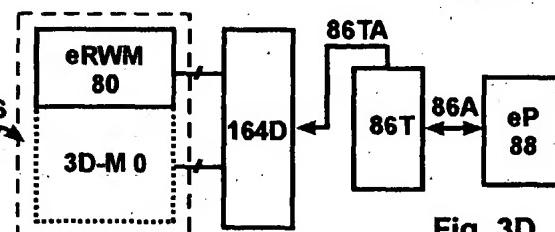


Fig. 3D

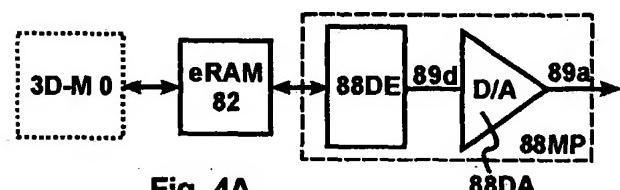


Fig. 4A

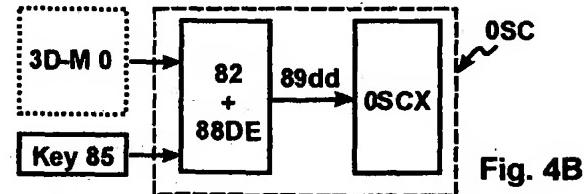


Fig. 4B

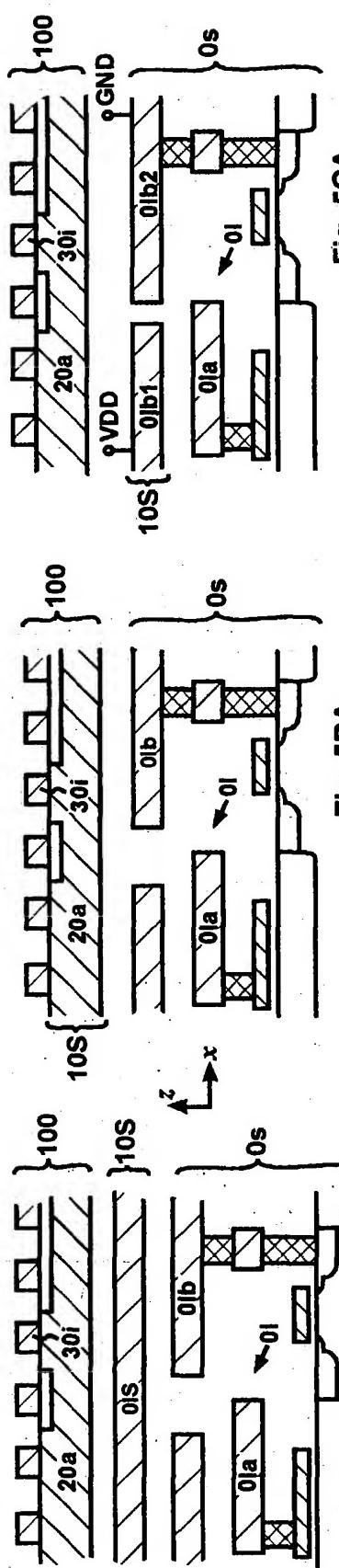


Fig. 5AA

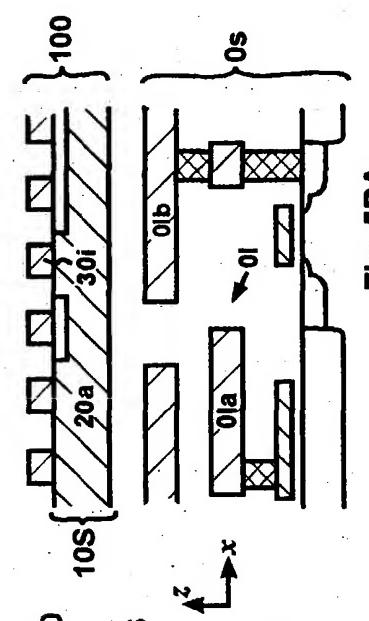


Fig. 5AB

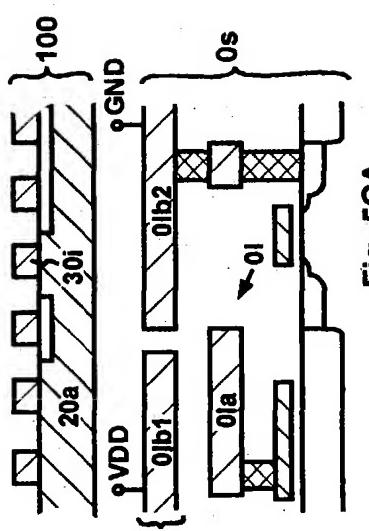


Fig. 5BA

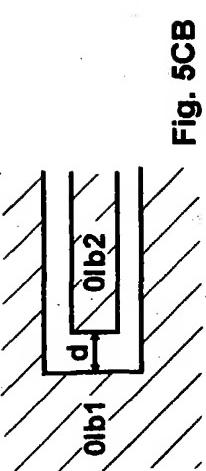


Fig. 5BB

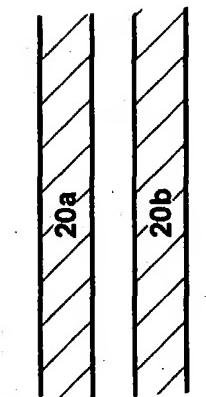


Fig. 5CA

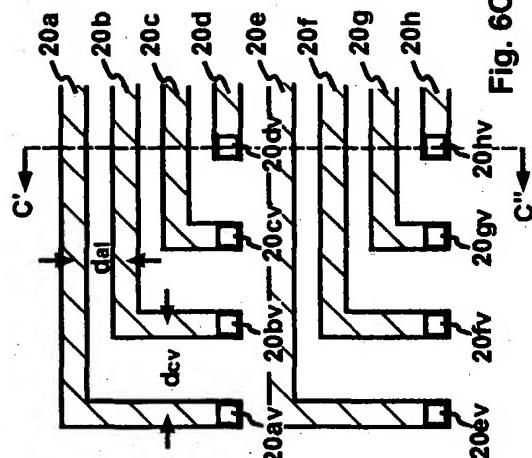


Fig. 5CB

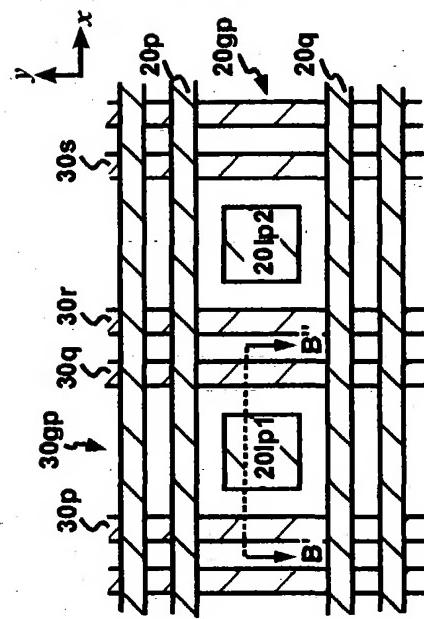


Fig. 6BA

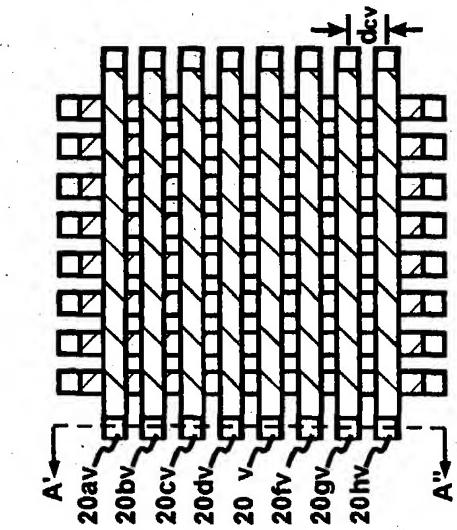
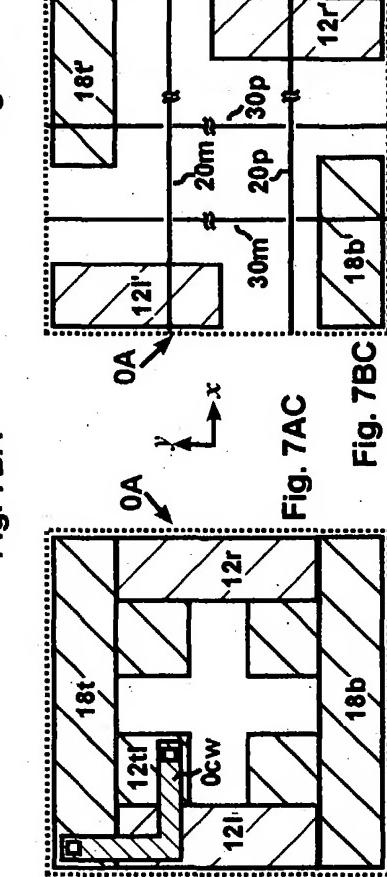
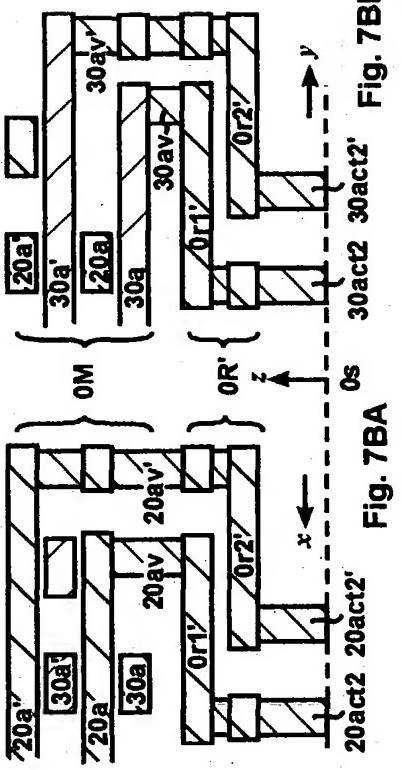
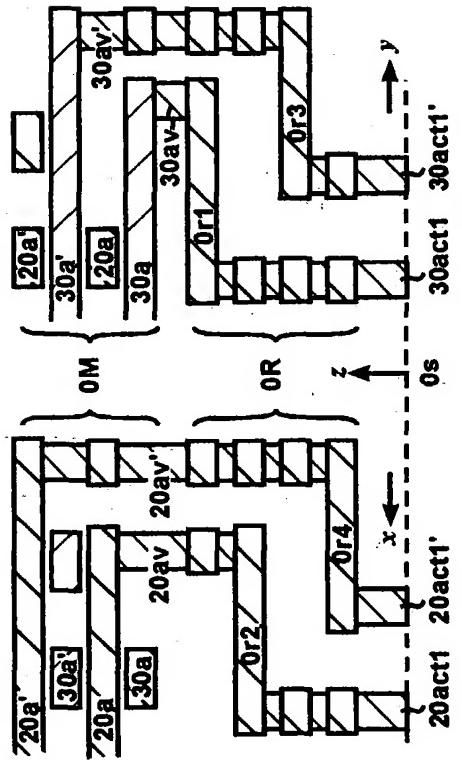
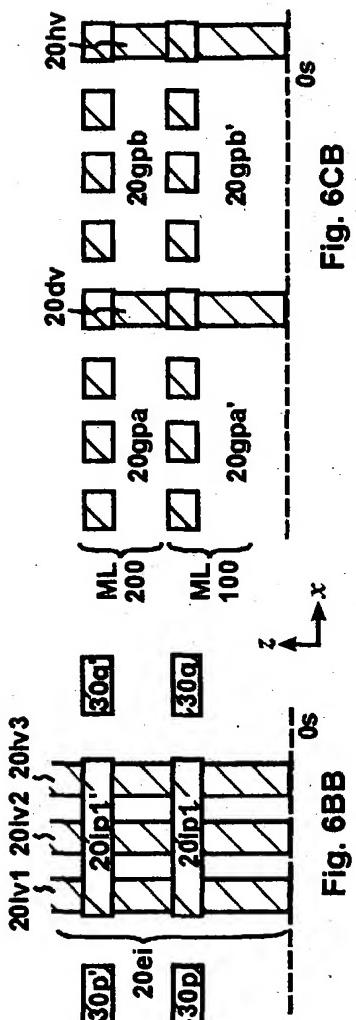
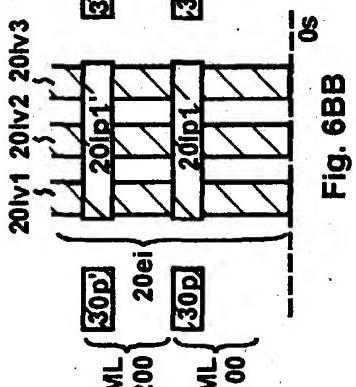
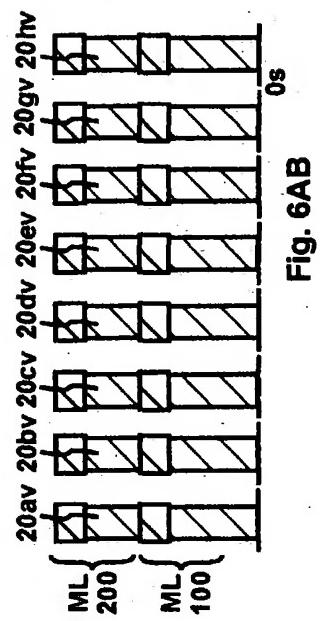


Fig. 6AA



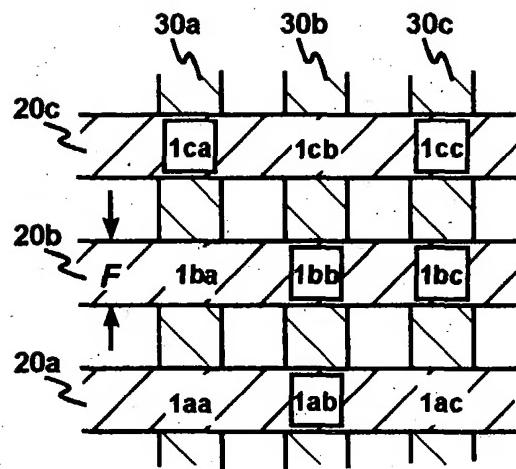


Fig. 8A

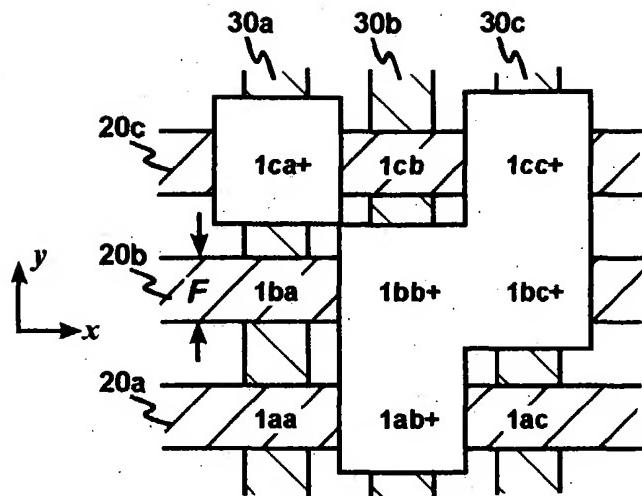


Fig. 8B

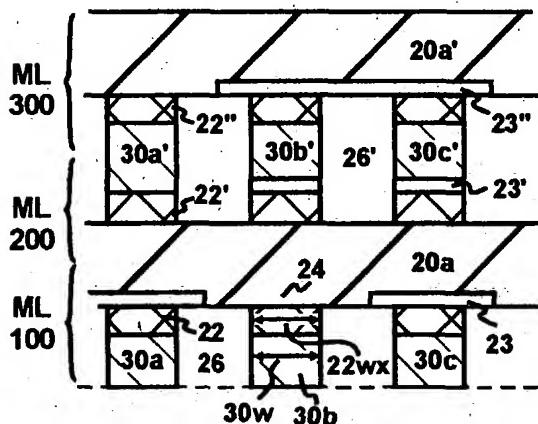


Fig. 9A

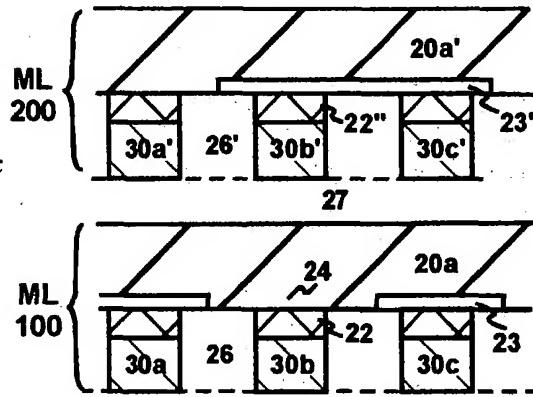


Fig. 9C



Fig. 9BA

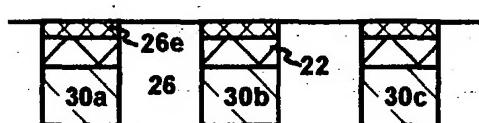


Fig. 9BA'

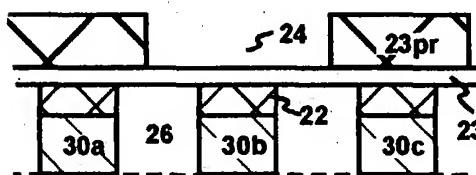


Fig. 9BB

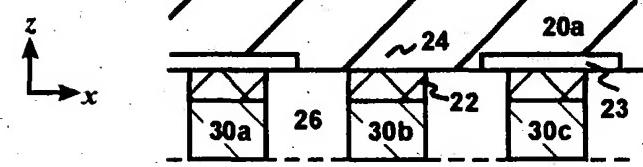


Fig. 9BC

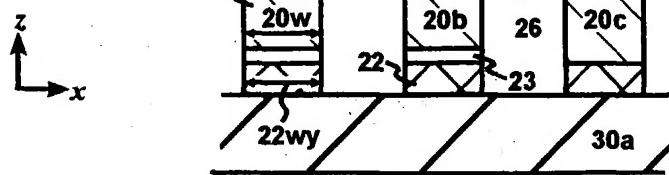


Fig. 9BD

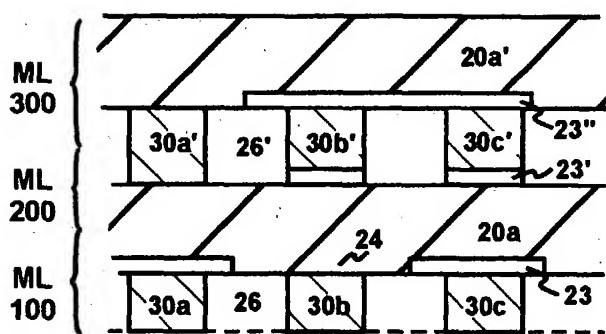


Fig. 10A

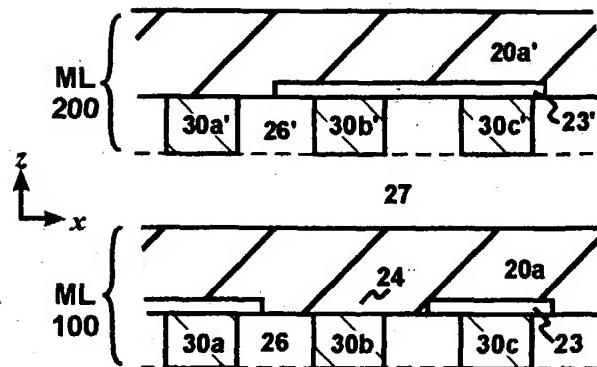


Fig. 10D

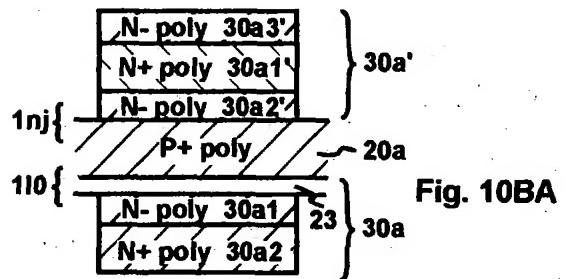


Fig. 10BA

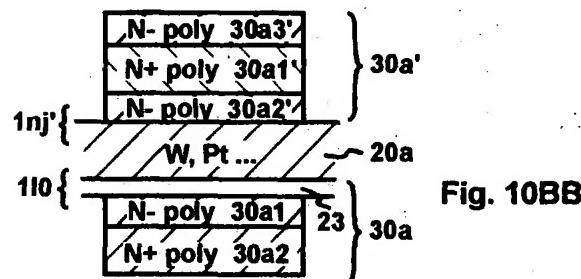


Fig. 10BB

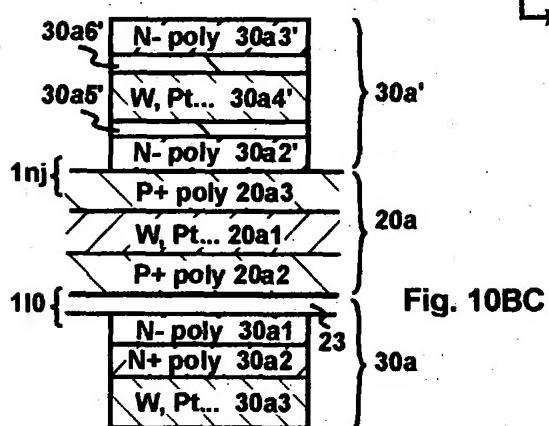


Fig. 10BC

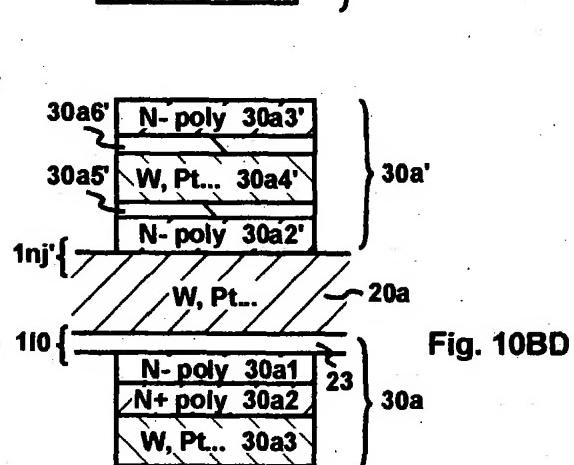


Fig. 10BD



Fig. 10CA

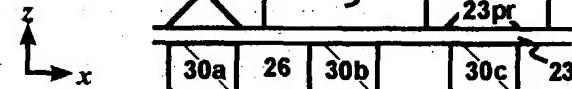


Fig. 10CB

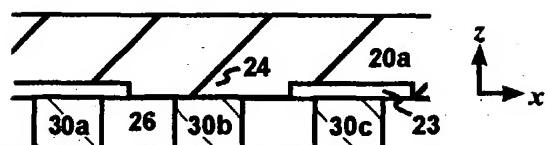


Fig. 10CC

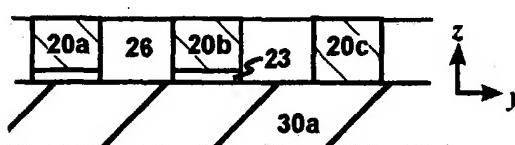


Fig. 10CD

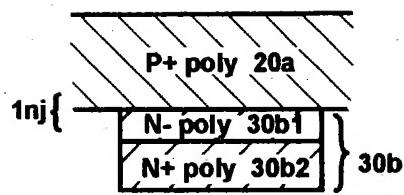


Fig. 10EA

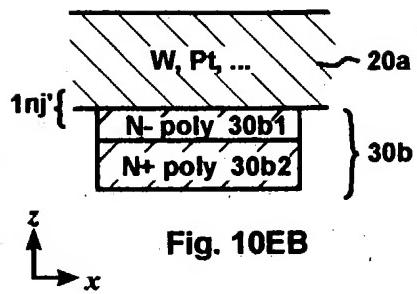


Fig. 10EB

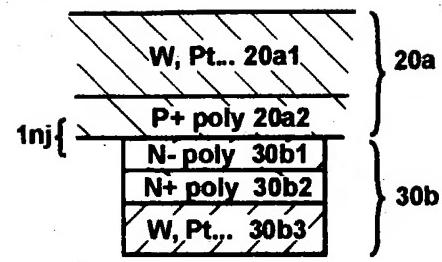


Fig. 10EC

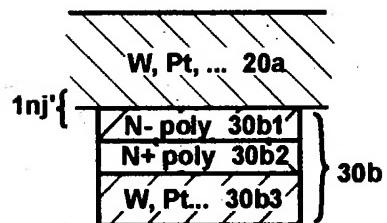


Fig. 10ED

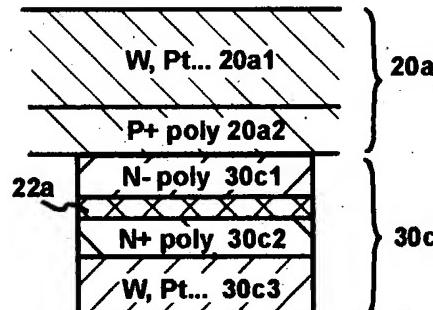


Fig. 11A

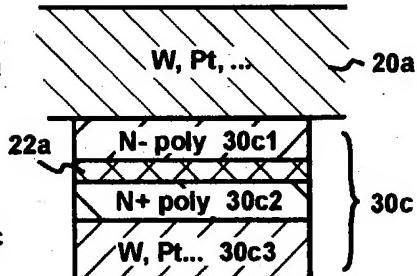


Fig. 11B

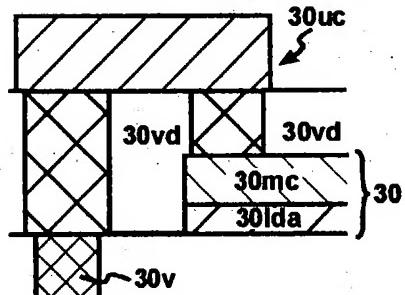


Fig. 12AA

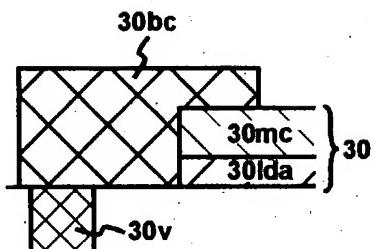


Fig. 12AB

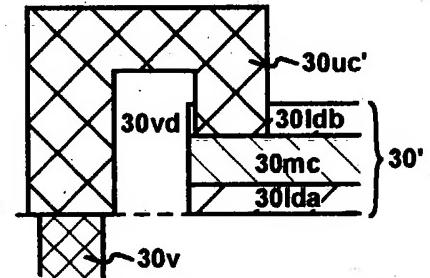


Fig. 12BA

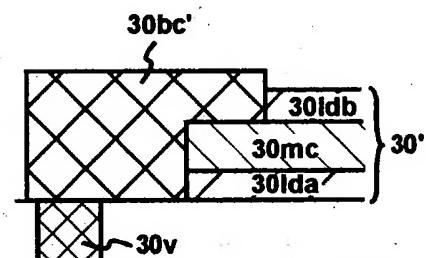


Fig. 12BB

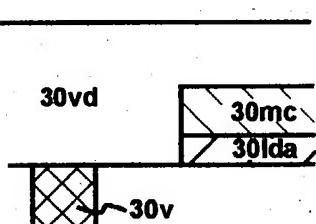


Fig. 12CA

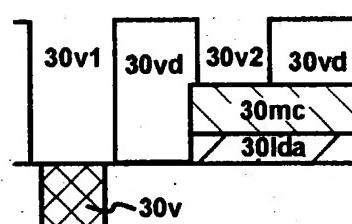


Fig. 12CB

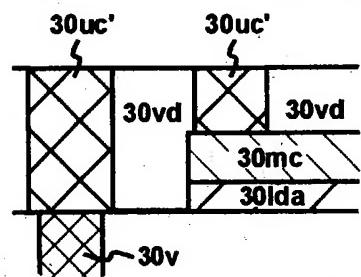


Fig. 12CC

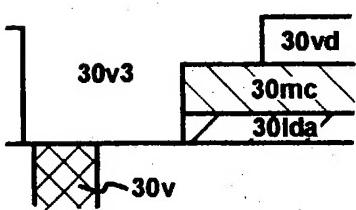


Fig. 12CB'

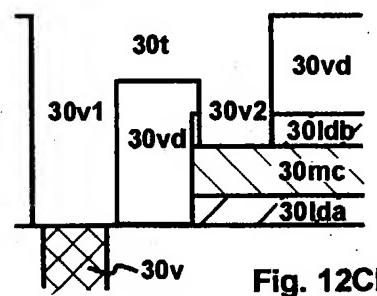


Fig. 12CB''

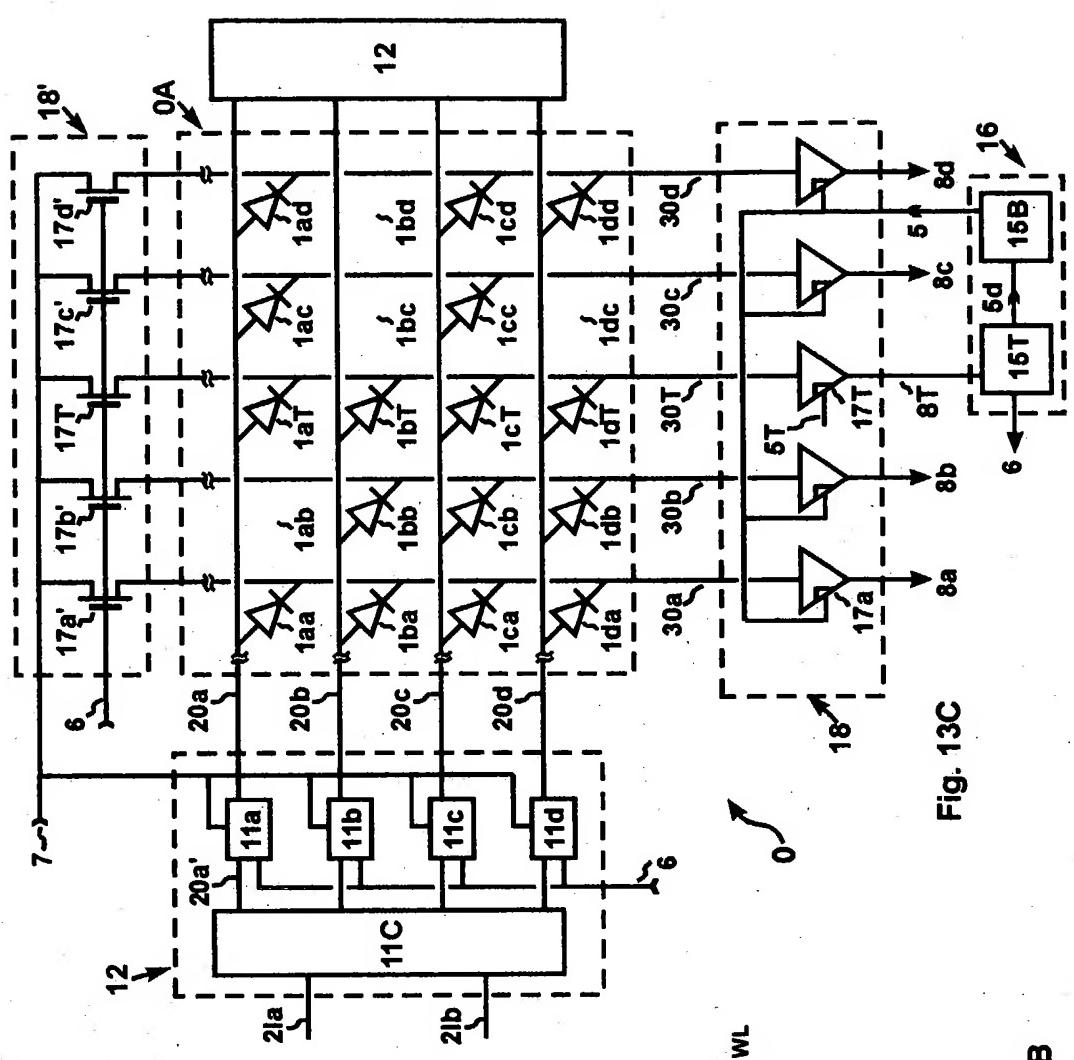


Fig. 13A

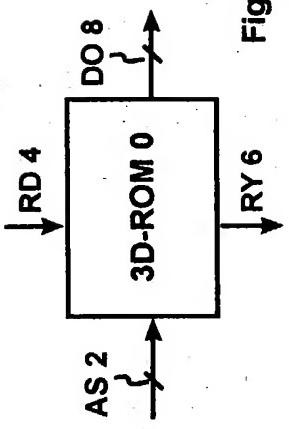


Fig. 13B

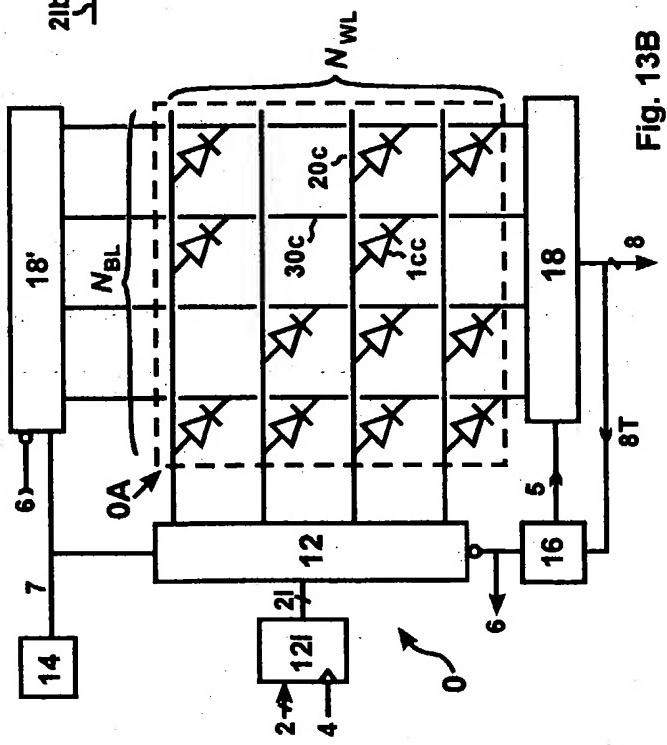


Fig. 13C

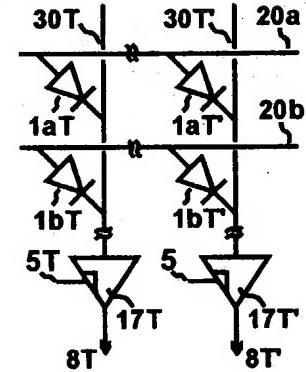
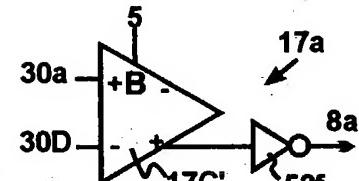
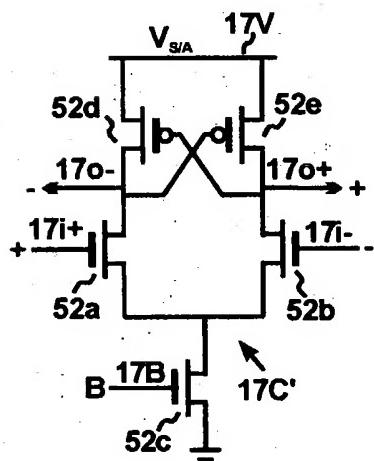
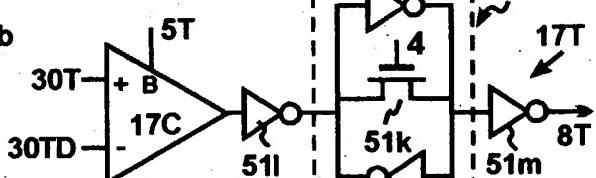
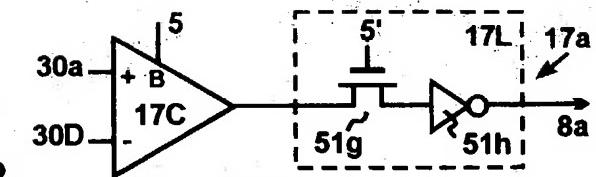
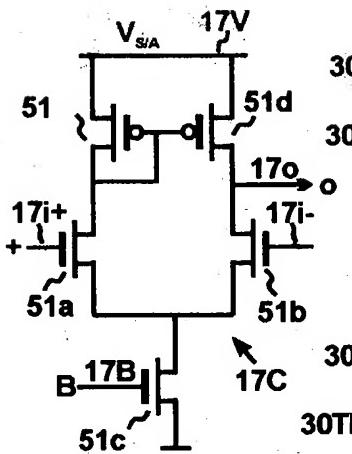
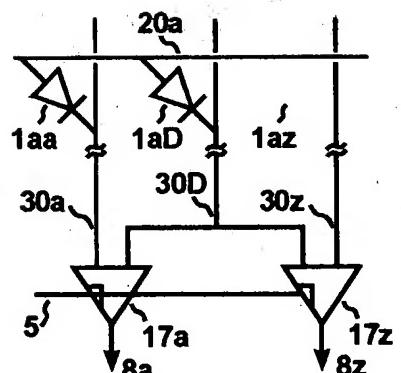
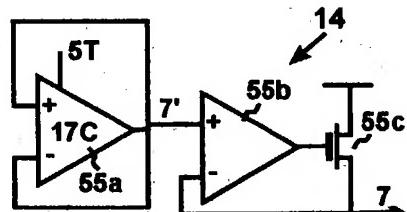
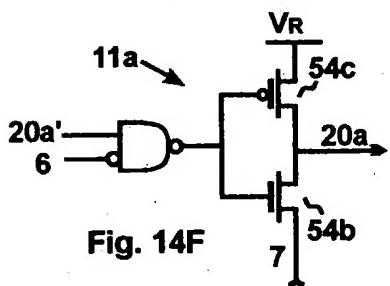
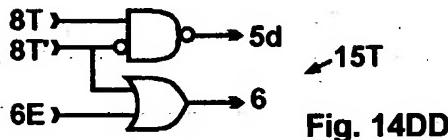
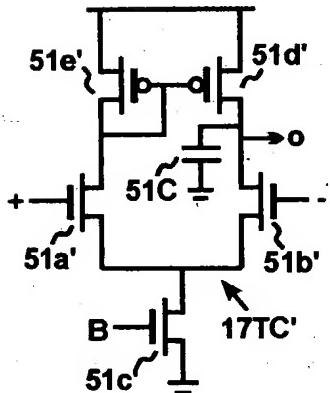
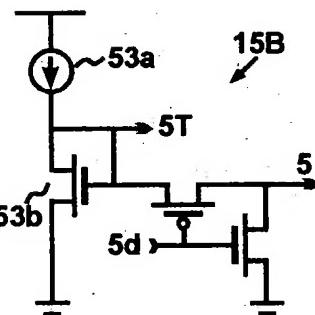
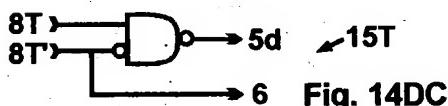


Fig. 14CC



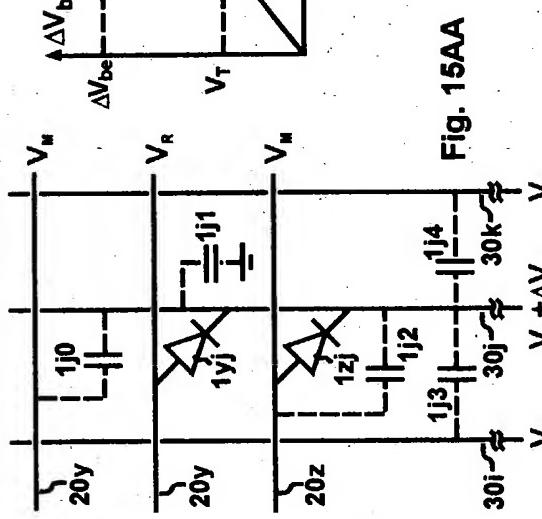


Fig. 15AA

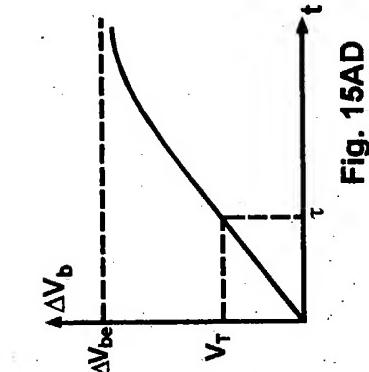


Fig. 15AD

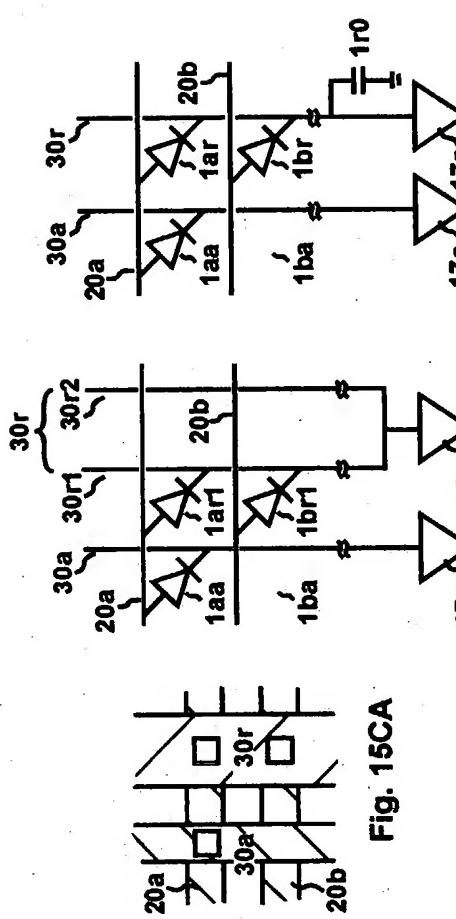


Fig. 15CC

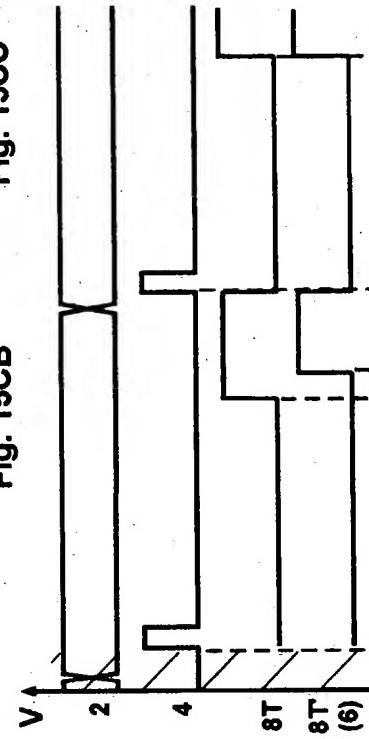


Fig. 15CB

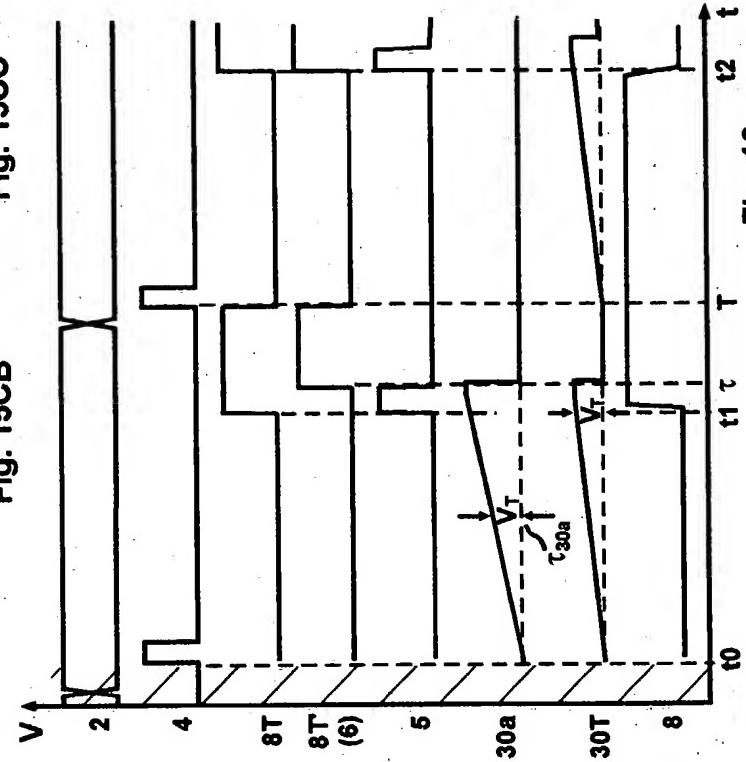


Fig. 15CC

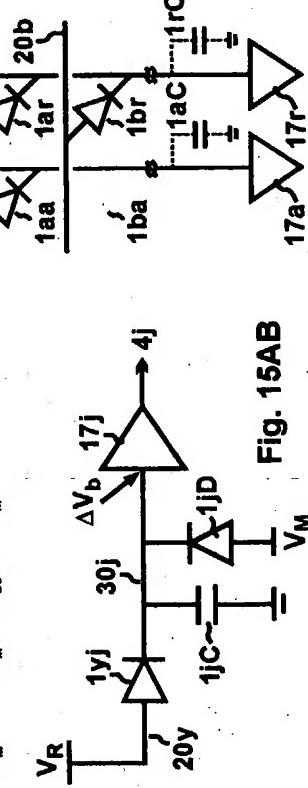


Fig. 15AB

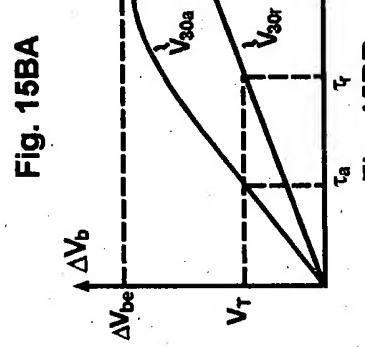


Fig. 15BA

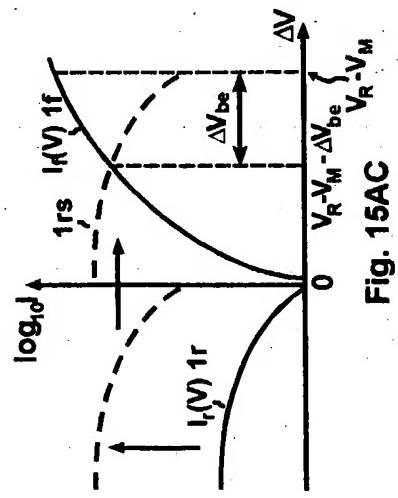


Fig. 15AC

Fig. 15BB

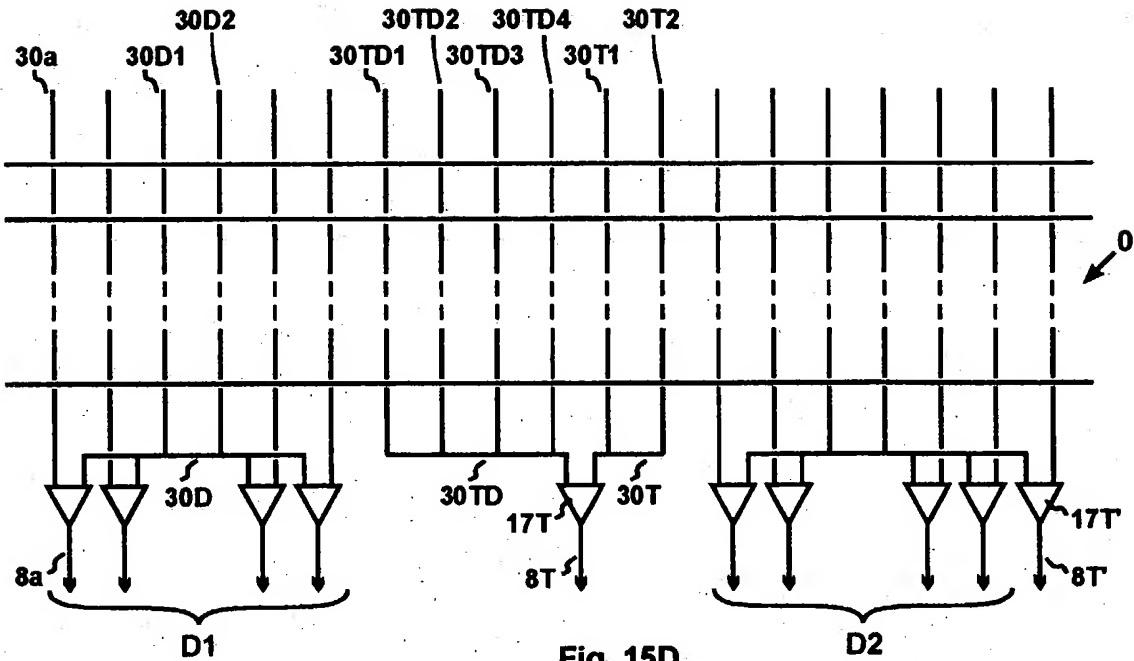


Fig. 15D

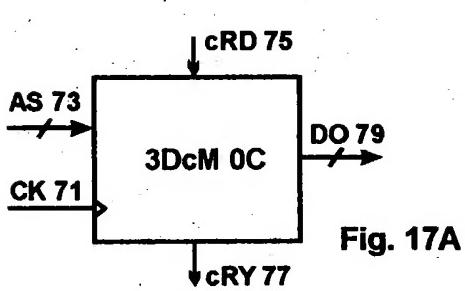


Fig. 17A

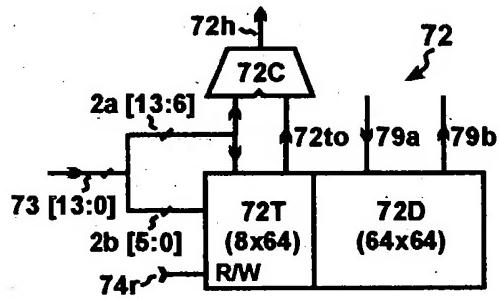


Fig. 17C

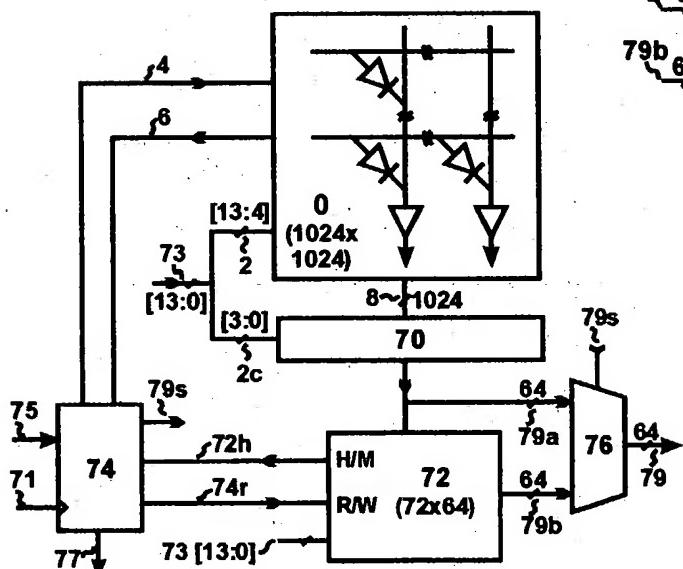


Fig. 17B

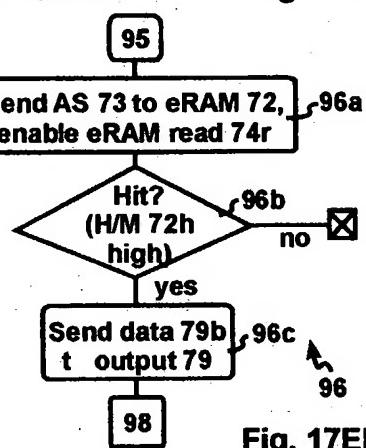


Fig. 17EB

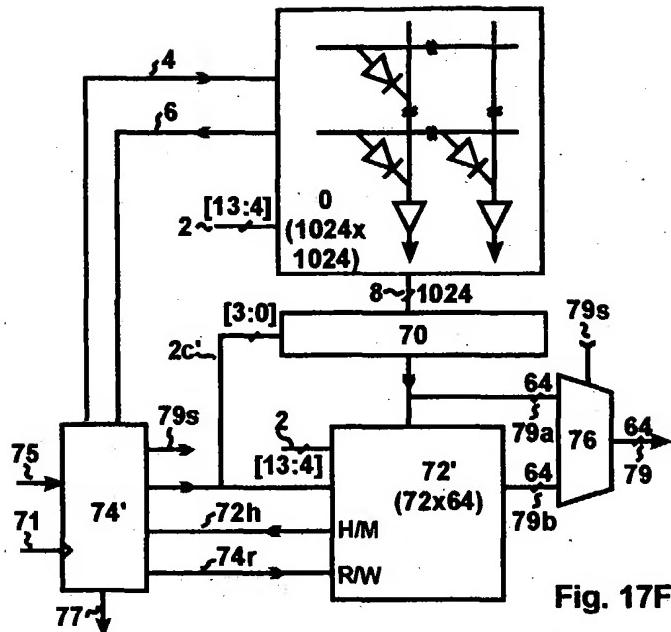
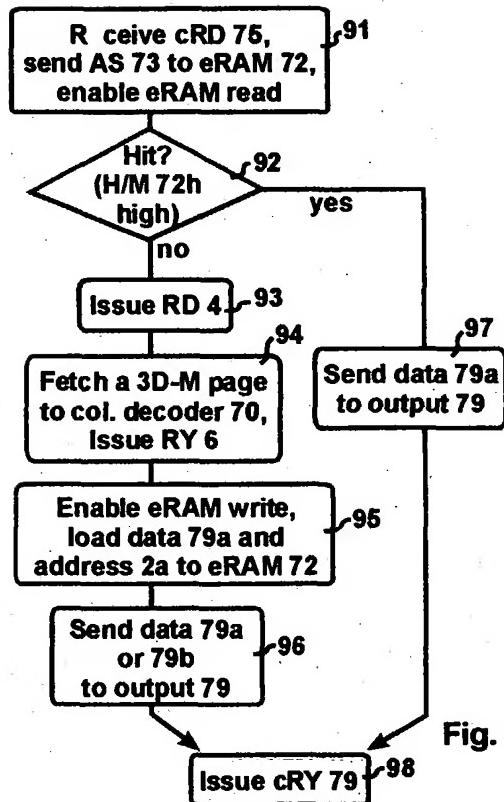


Fig. 17F

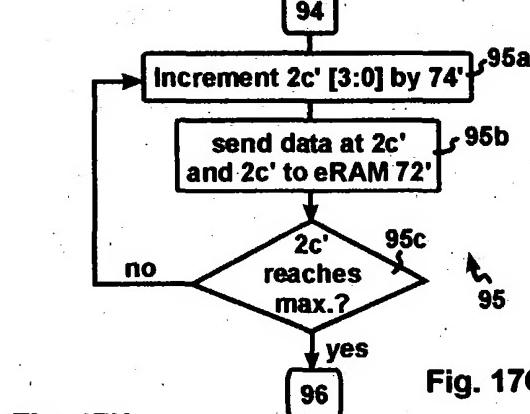
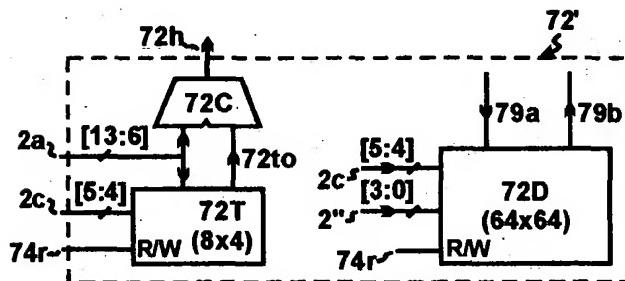


Fig. 17H

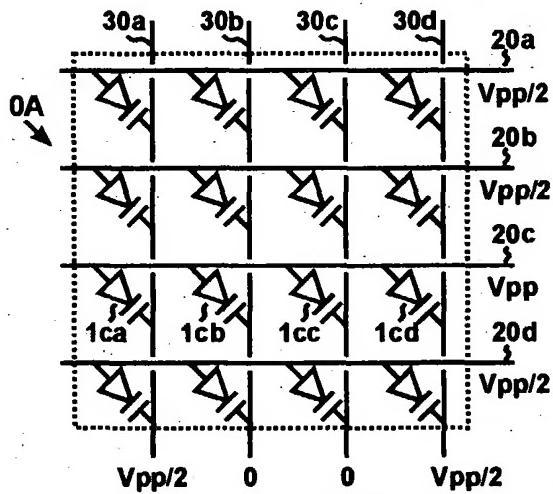


Fig. 18A

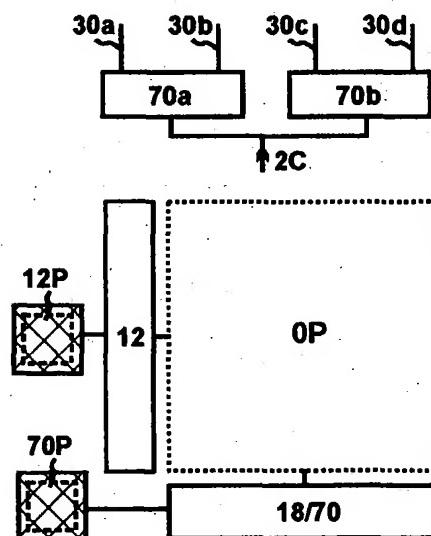


Fig. 18C

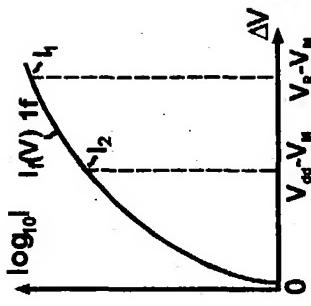


Fig. 19CA

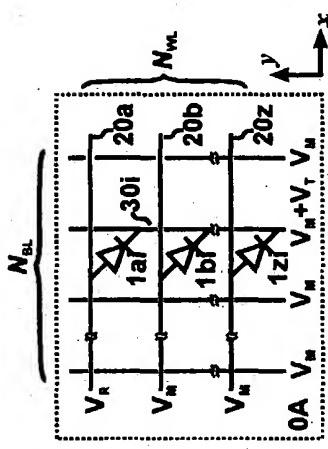


Fig. 19B

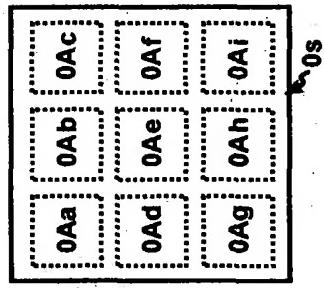


Fig. 19AB

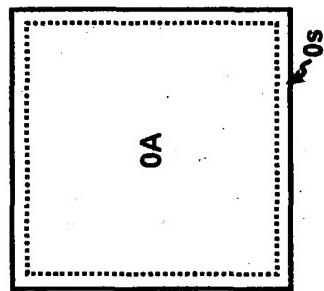


Fig. 19AA

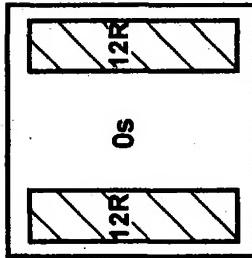


Fig. 19CC

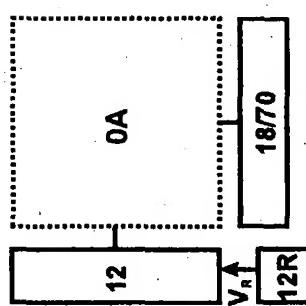


Fig. 19CB

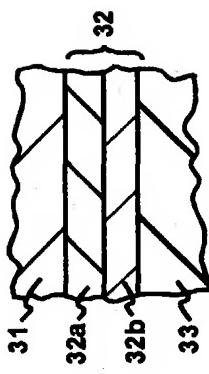


Fig. 19EA

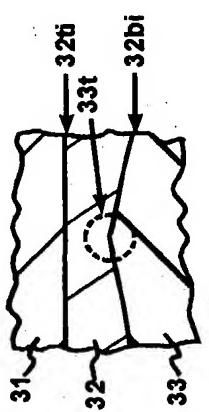
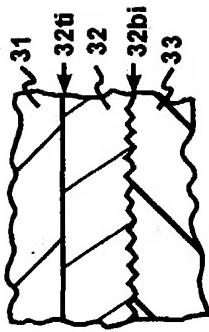


Fig. 19F

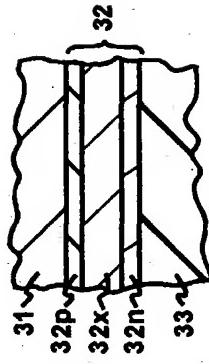


Fig. 19EC

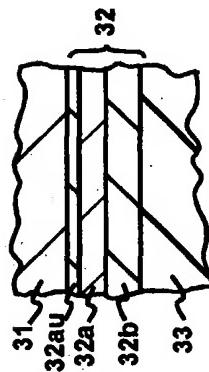


Fig. 19EB

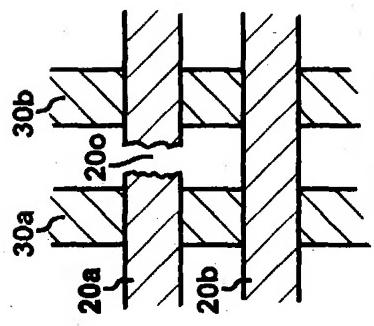


Fig. 20AA

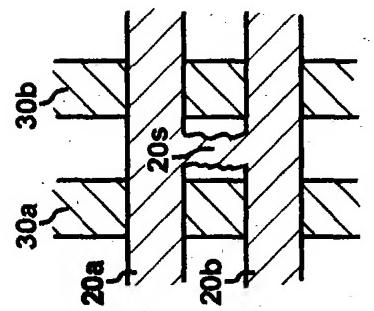


Fig. 20AB

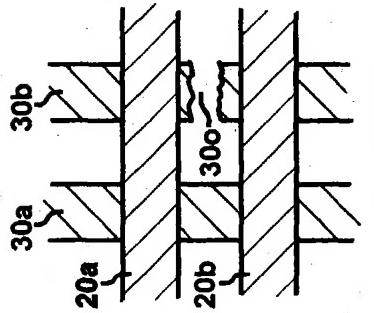


Fig. 20BA

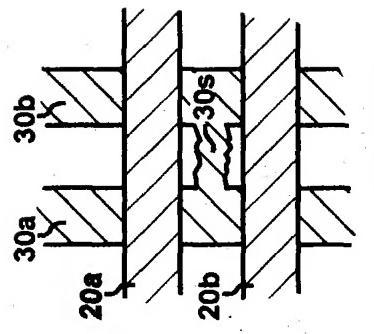


Fig. 20BB

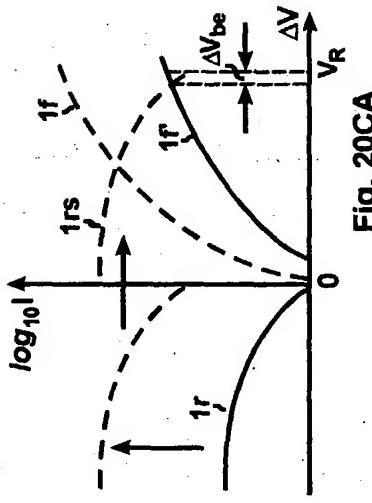


Fig. 20CA

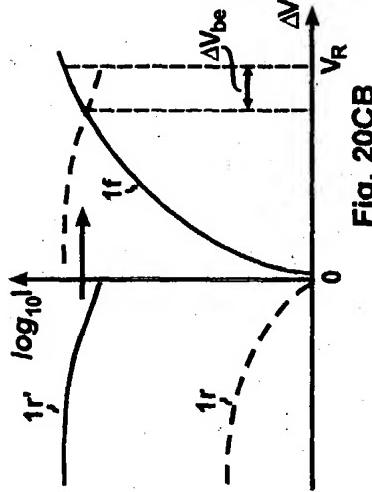


Fig. 20CB

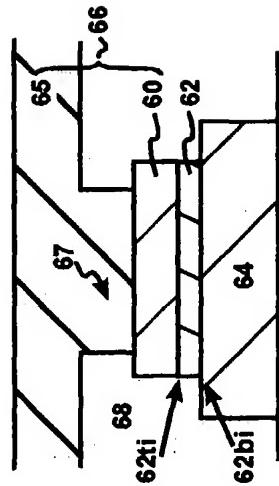


Fig. 21A

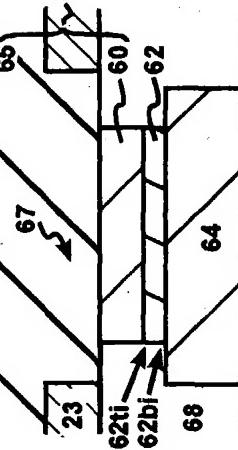


Fig. 21B

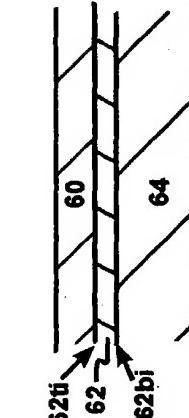


Fig. 22AA

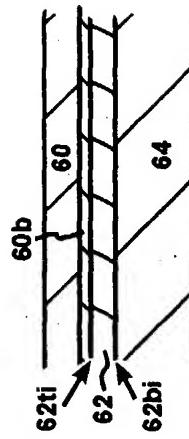


Fig. 22AB

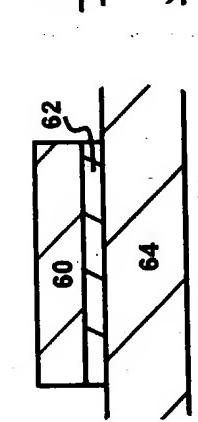


Fig. 22BA

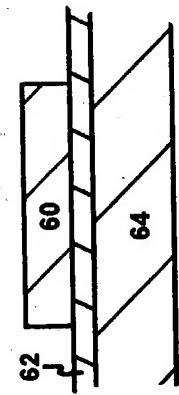


Fig. 22BB

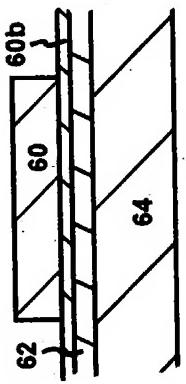


Fig. 22BC

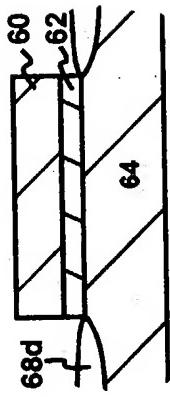


Fig. 22CA

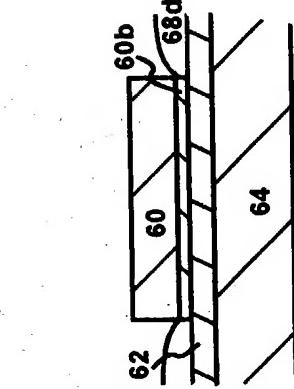


Fig. 22CC

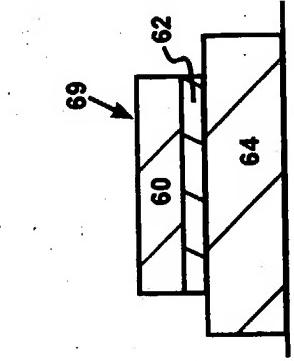


Fig. 22D

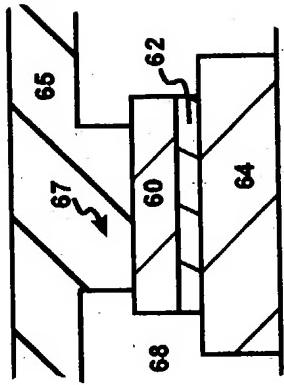


Fig. 22E

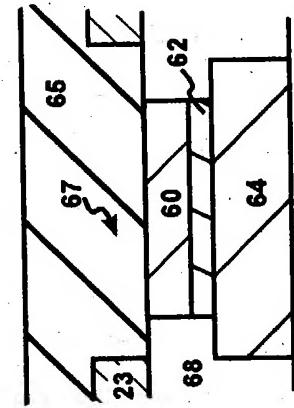


Fig. 22D'

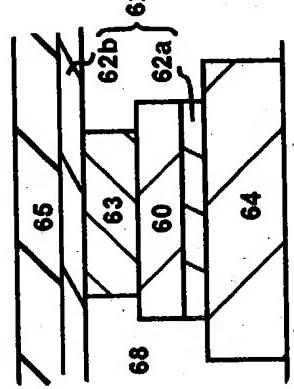


Fig. 22E'

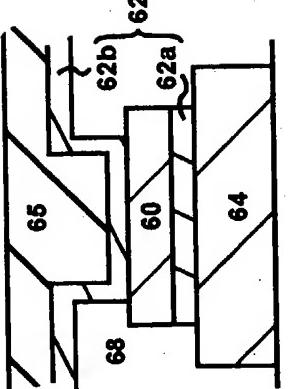


Fig. 23B

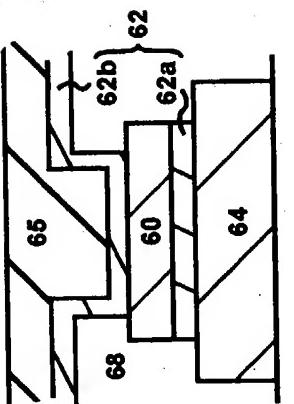


Fig. 23A

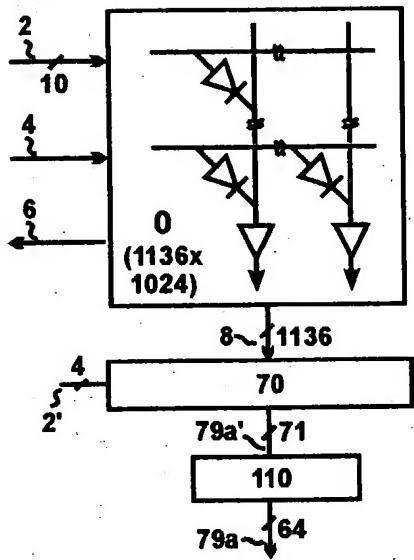


Fig. 24

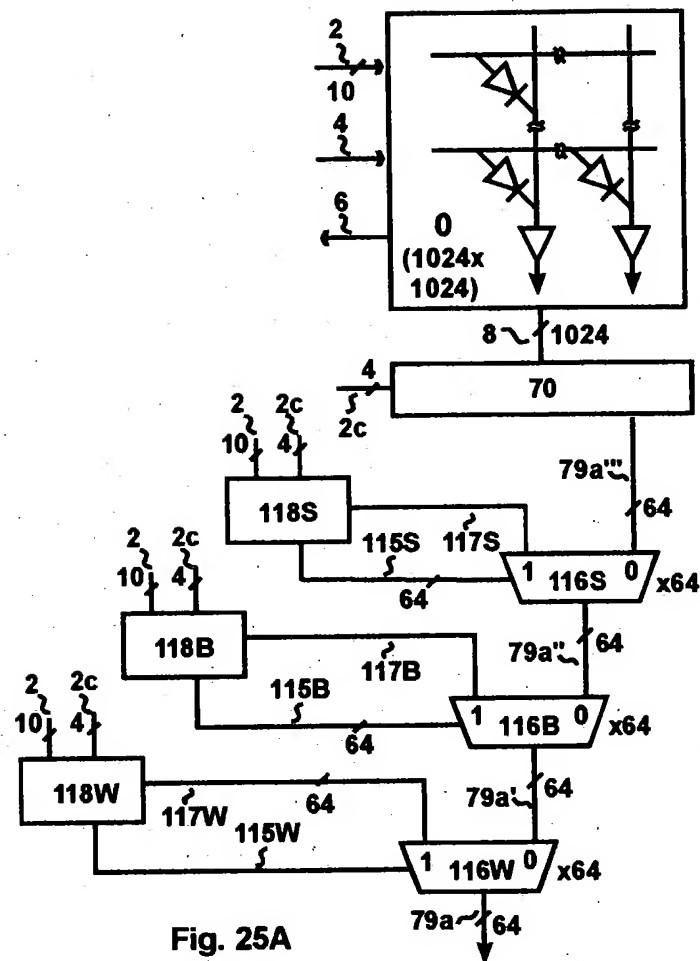


Fig. 25A

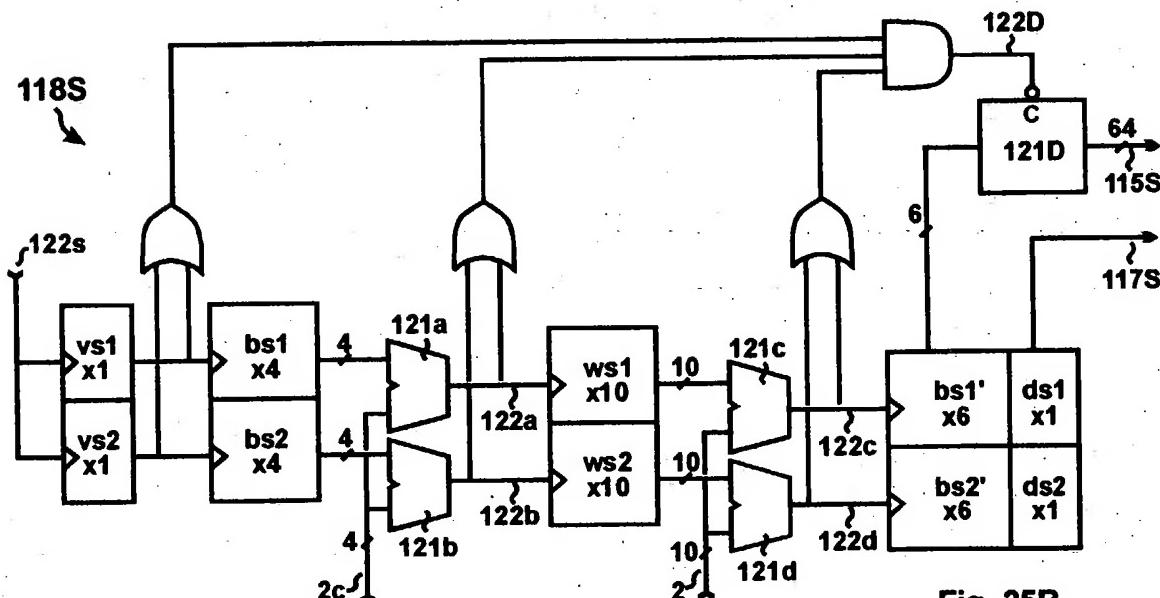


Fig. 25B

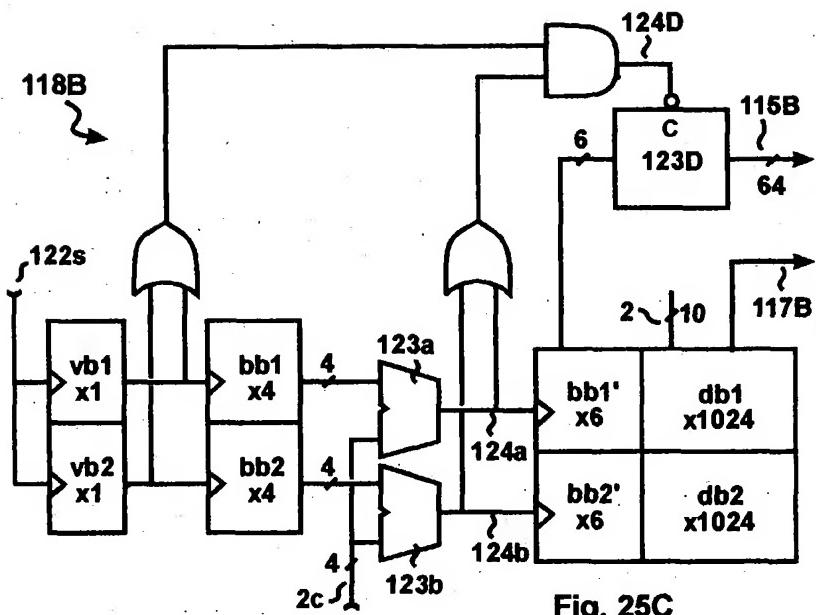


Fig. 25C

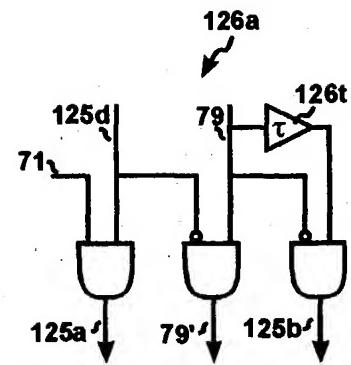


Fig. 25DB

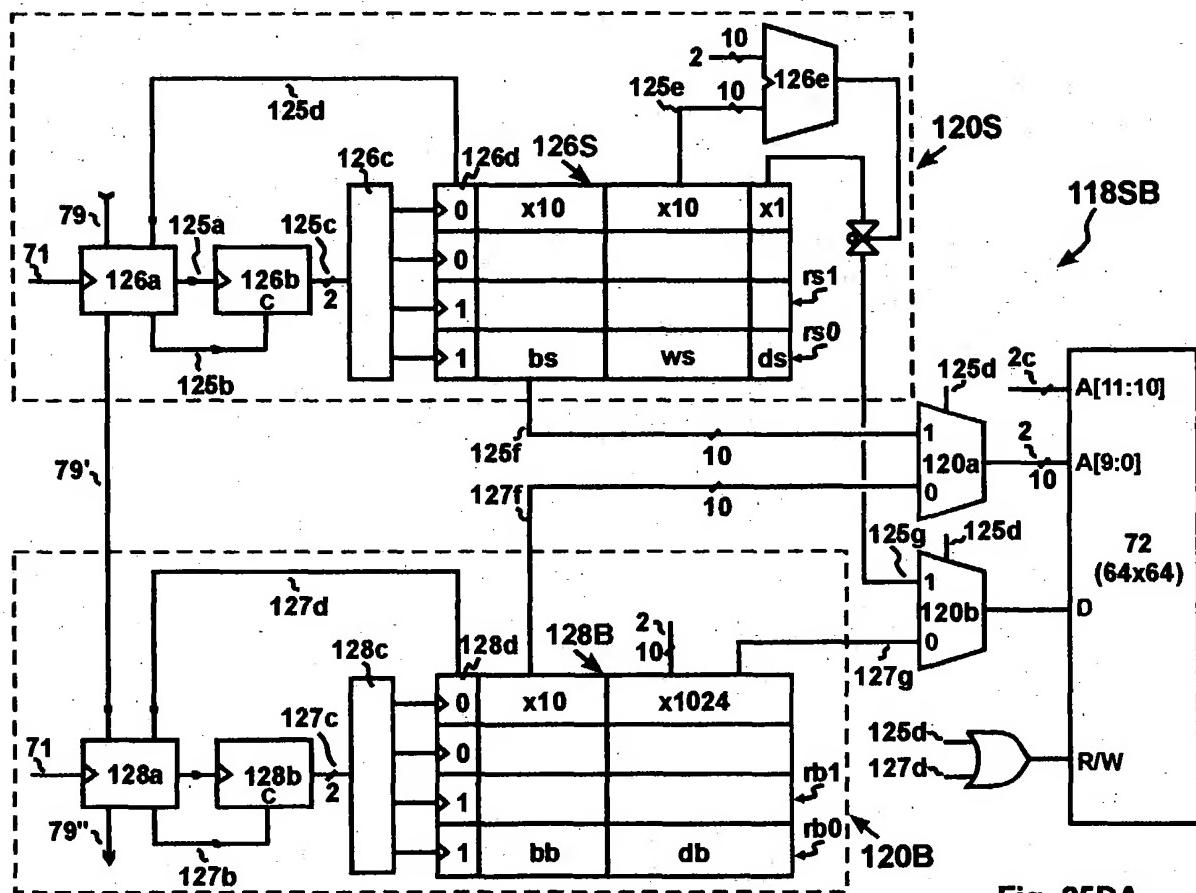


Fig. 25DA

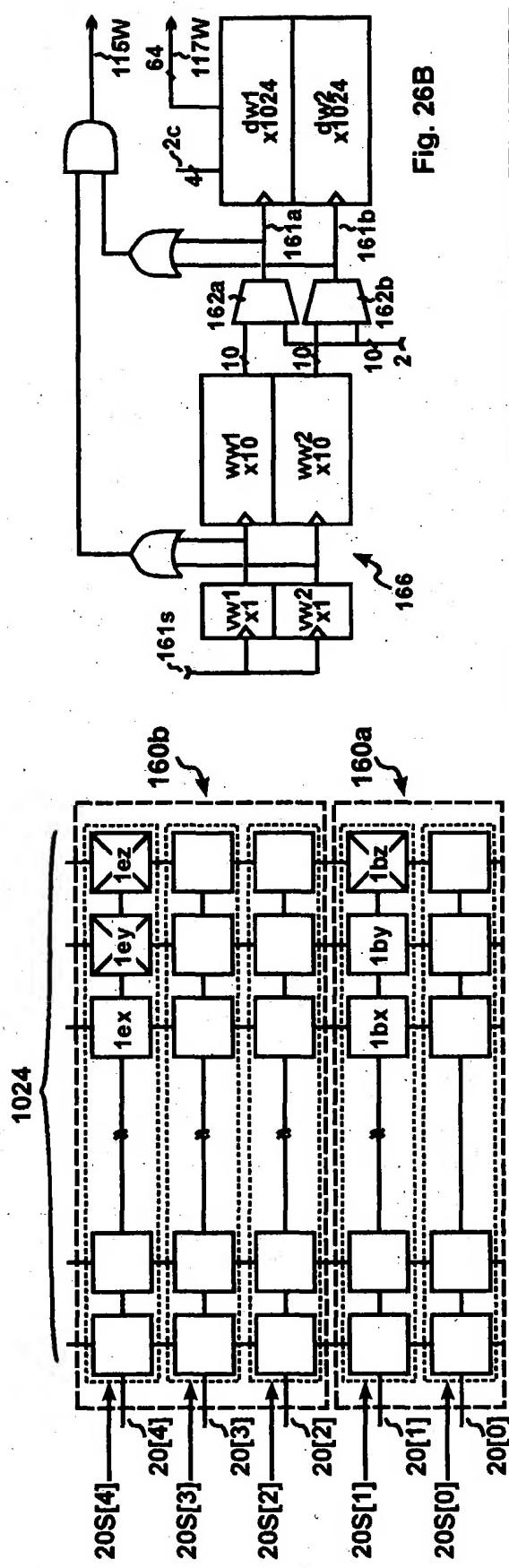


Fig. 26B

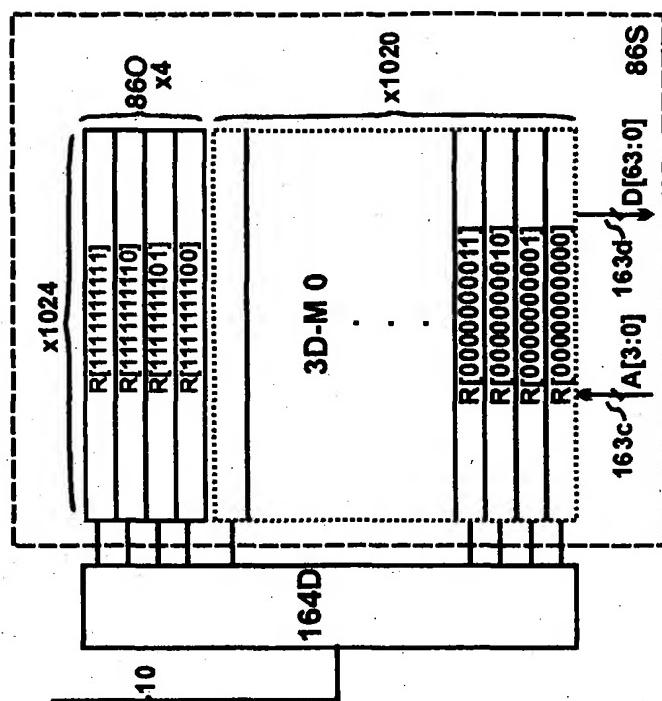


Fig. 26A

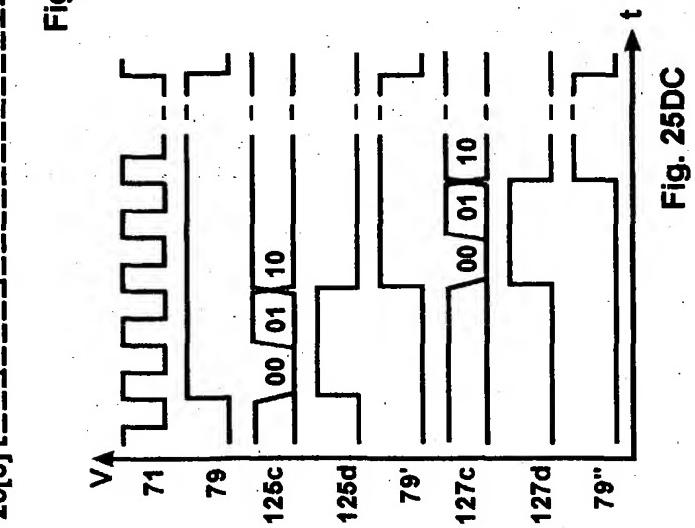


Fig. 26C

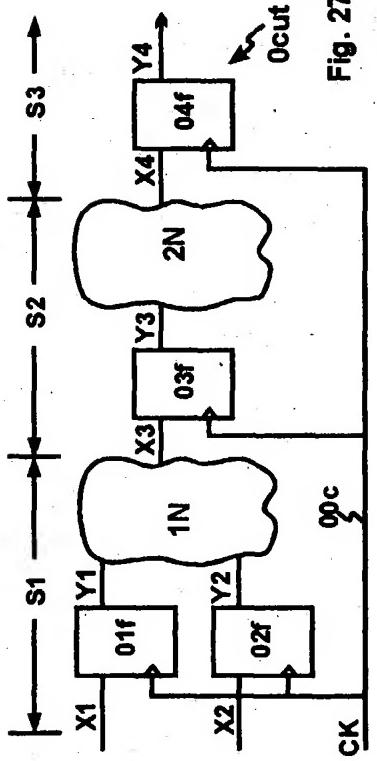


Fig. 27A

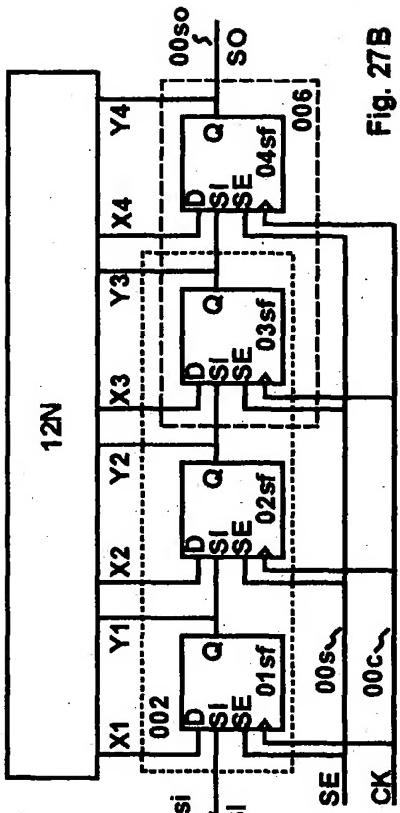


Fig. 27B

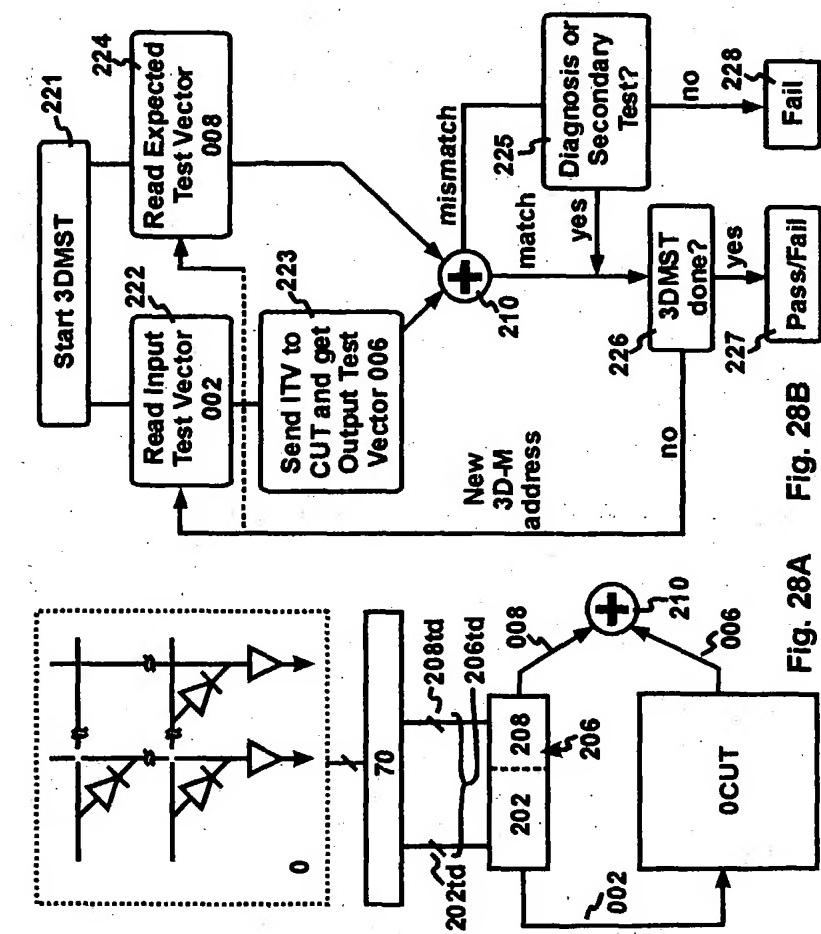


Fig. 28A

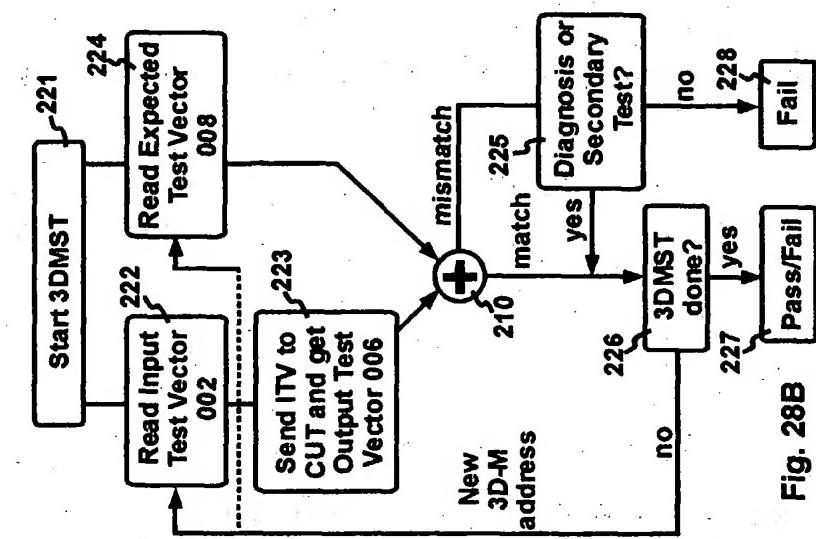


Fig. 28B Pass/Fail

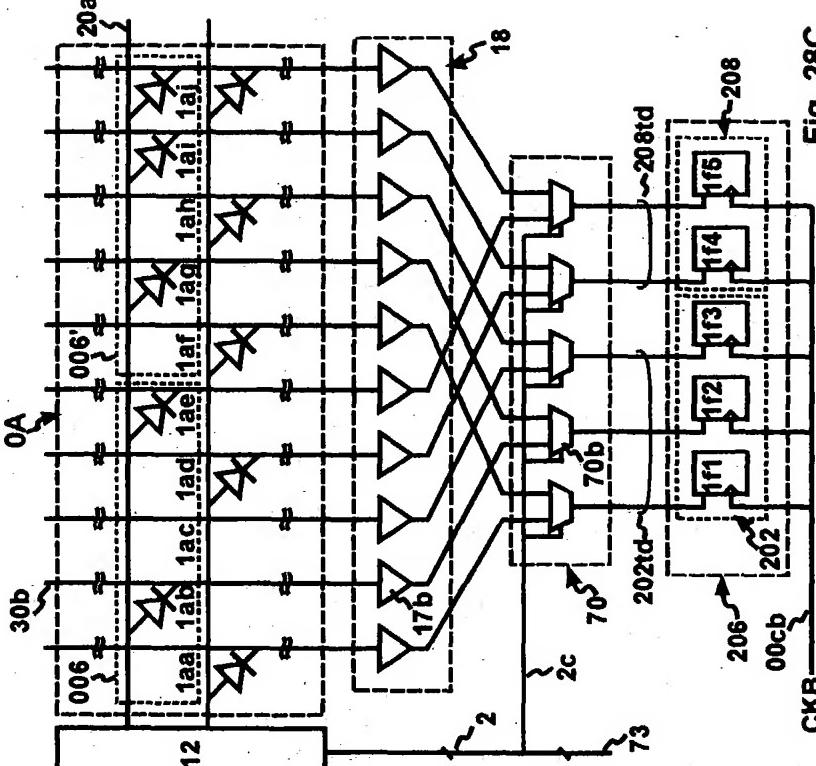
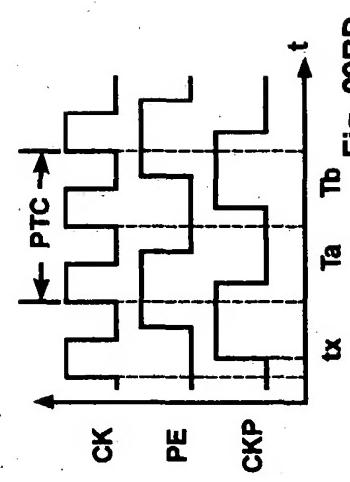
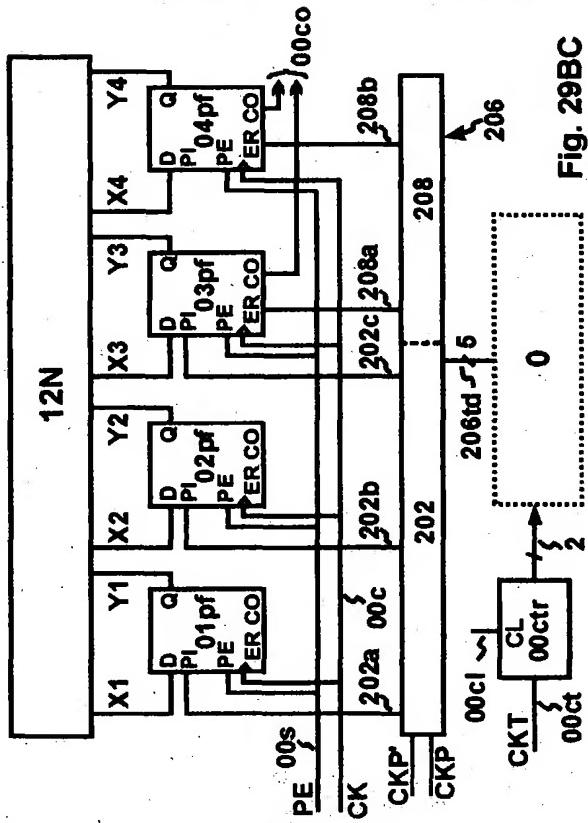
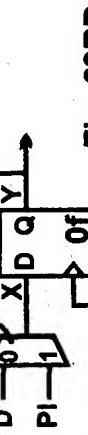
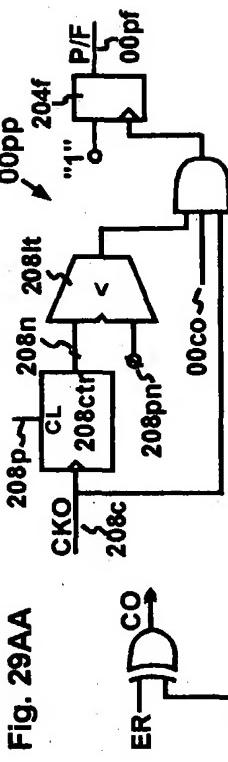
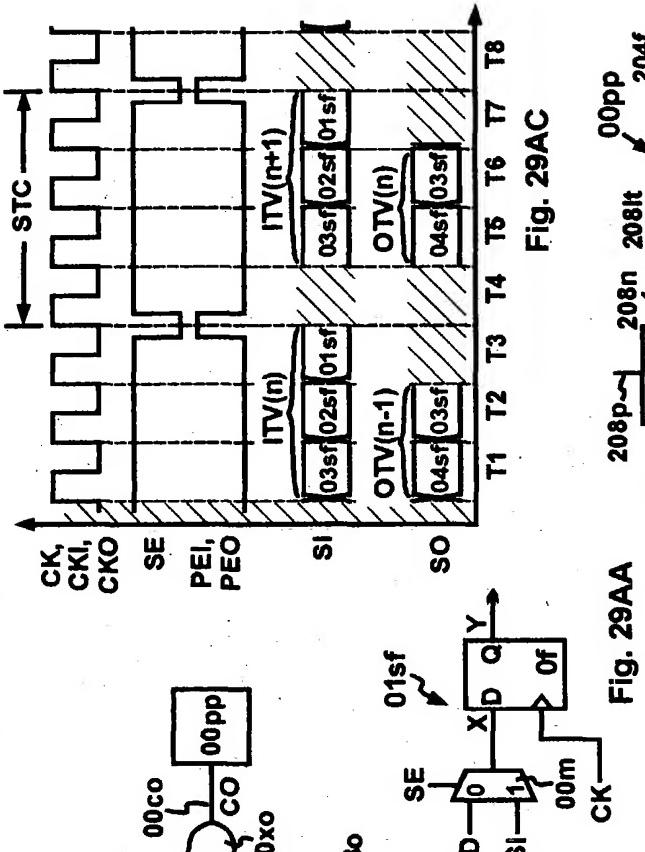
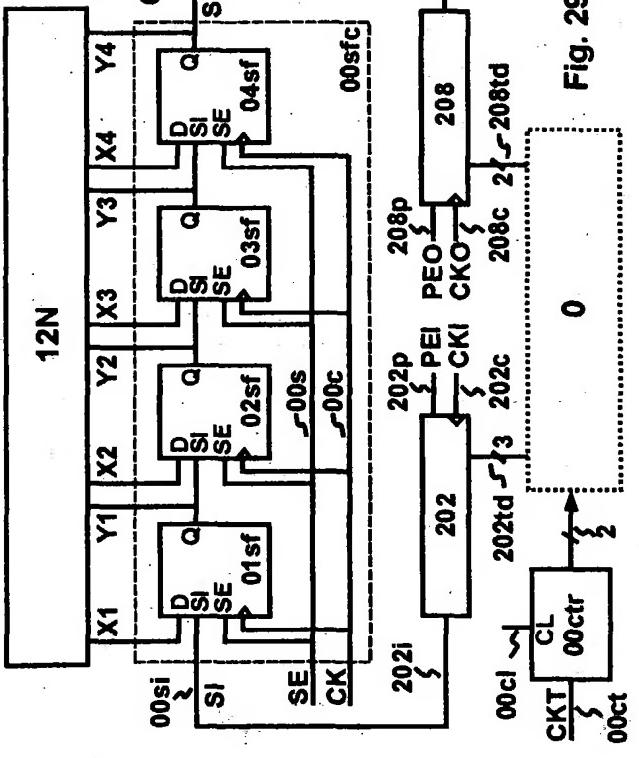


Fig. 28C



tx Ta Tb Fig. 29BD

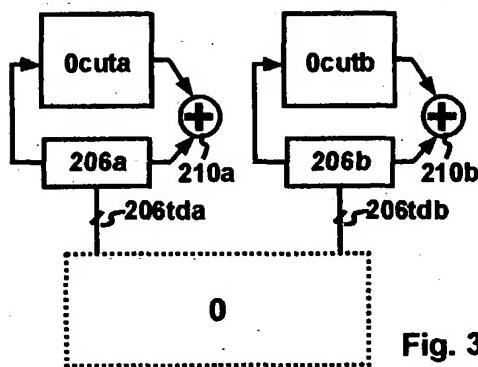


Fig. 30A

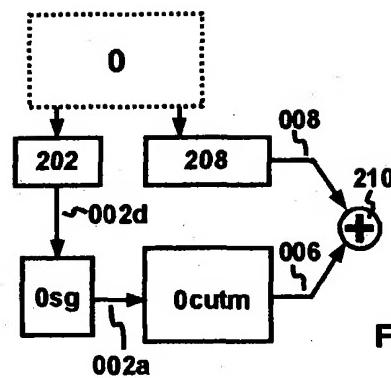


Fig. 30BA

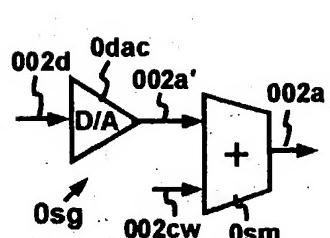


Fig. 30BB

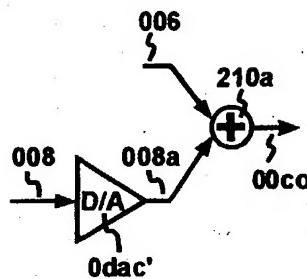


Fig. 30BC

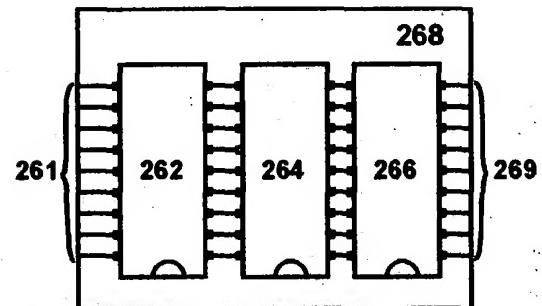


Fig. 30C

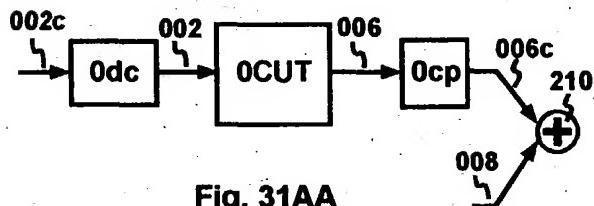


Fig. 31AA

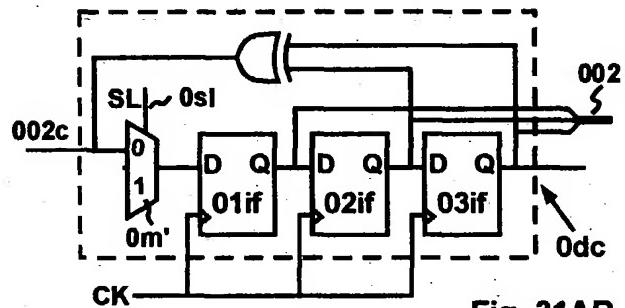


Fig. 31AB

	Test Methodology
Block-level test	BIST, etc.
Chip-level test/ Structural test	3DMST (optionally with EST)

Fig. 31BA

	Test Methodology
High-speed test, critical test vectors	3DMST (optionally with BIST)
Medium-to low-speed test	EST

Fig. 31BB

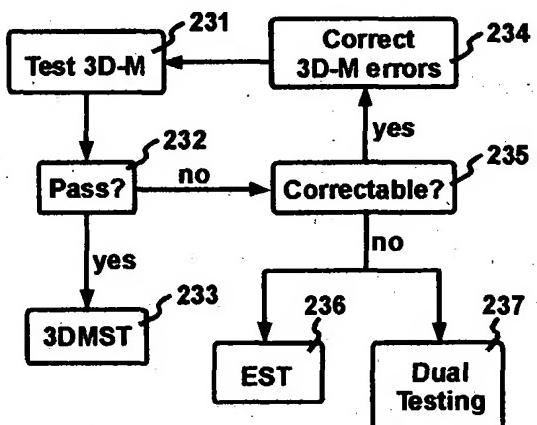


Fig. 32

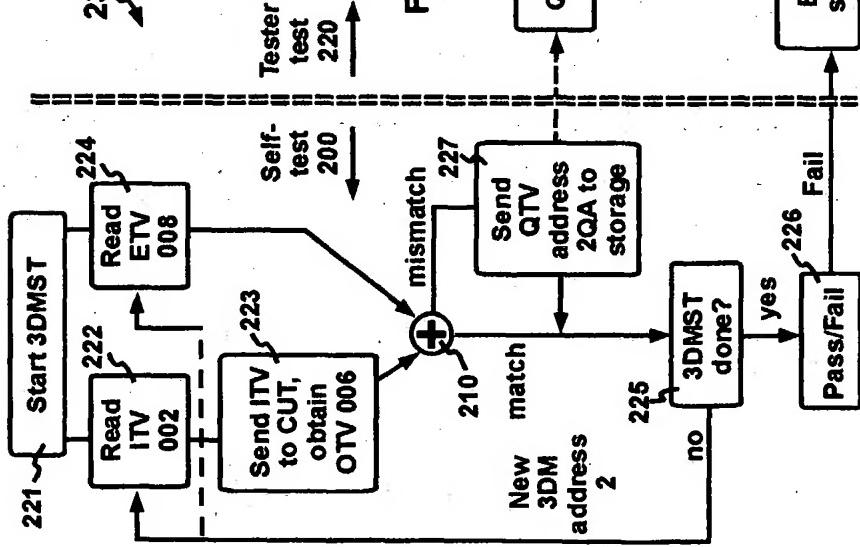


Fig. 33A

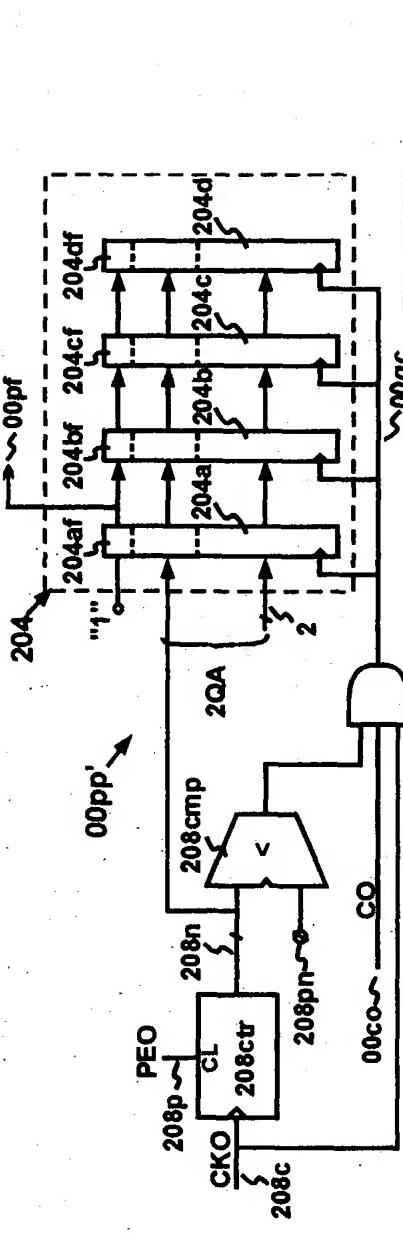


Fig. 33BB

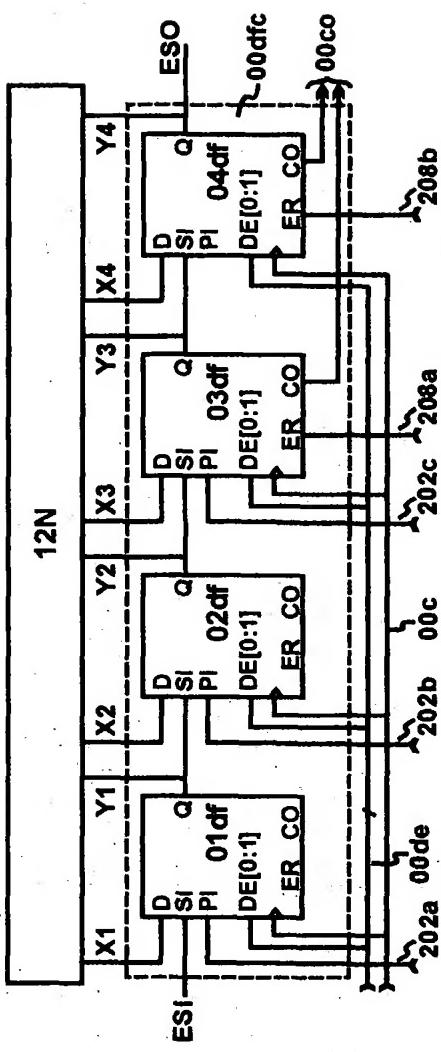


Fig. 33CA

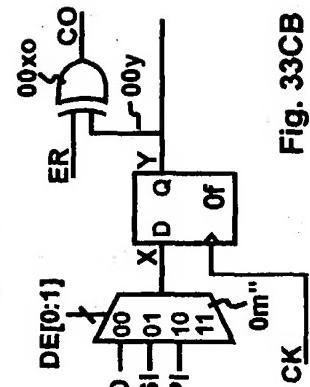


Fig. 33CB

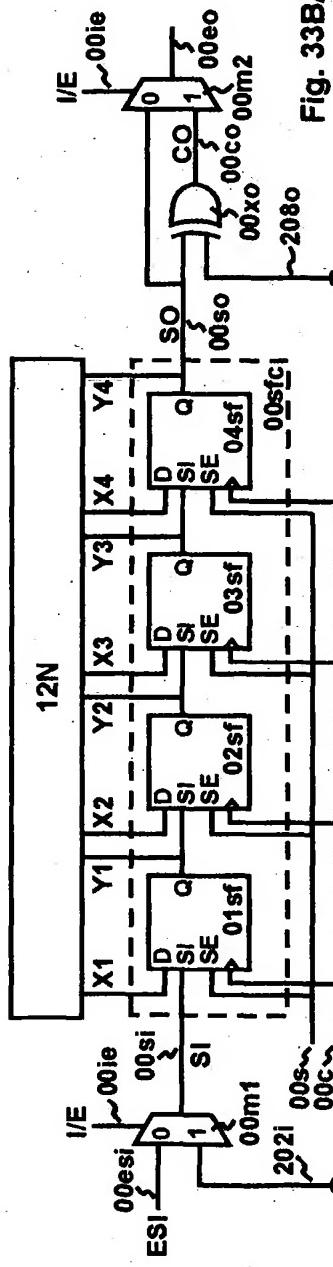


Fig. 33BA